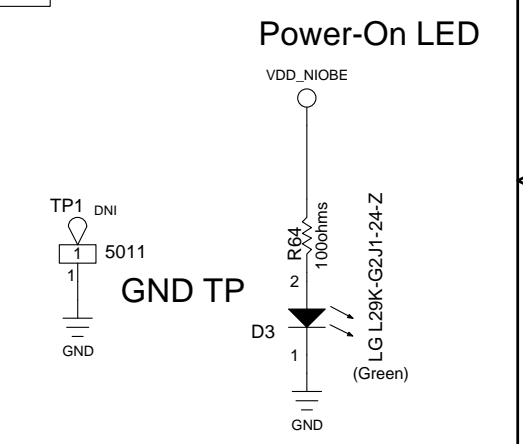
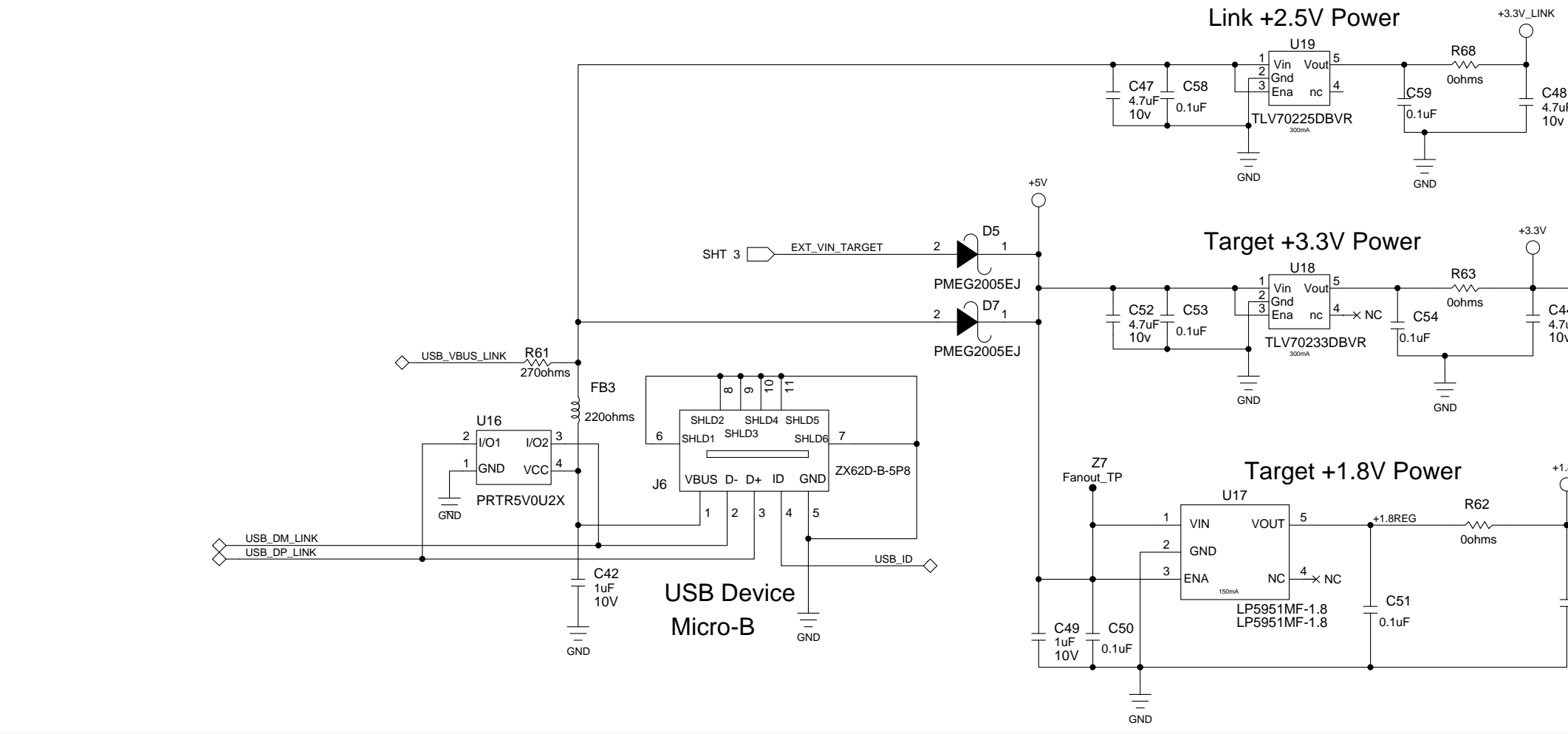
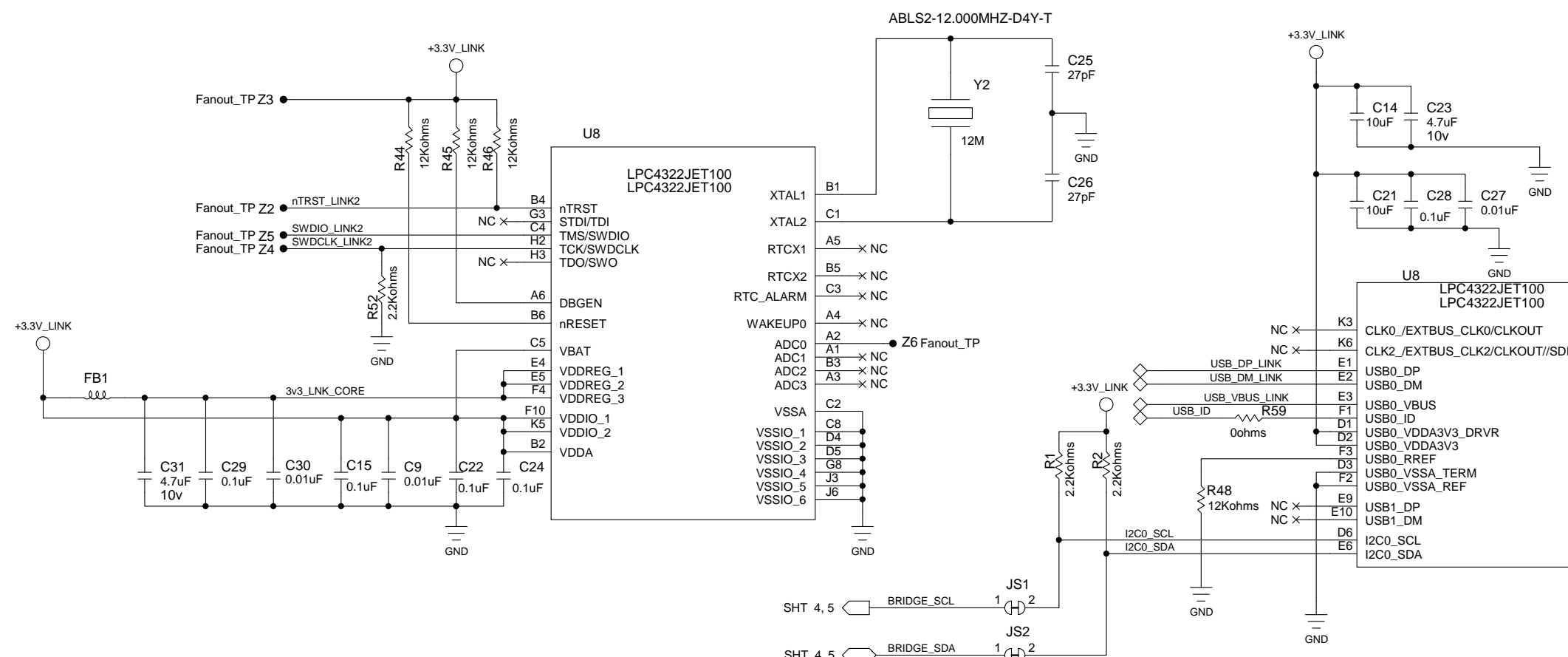


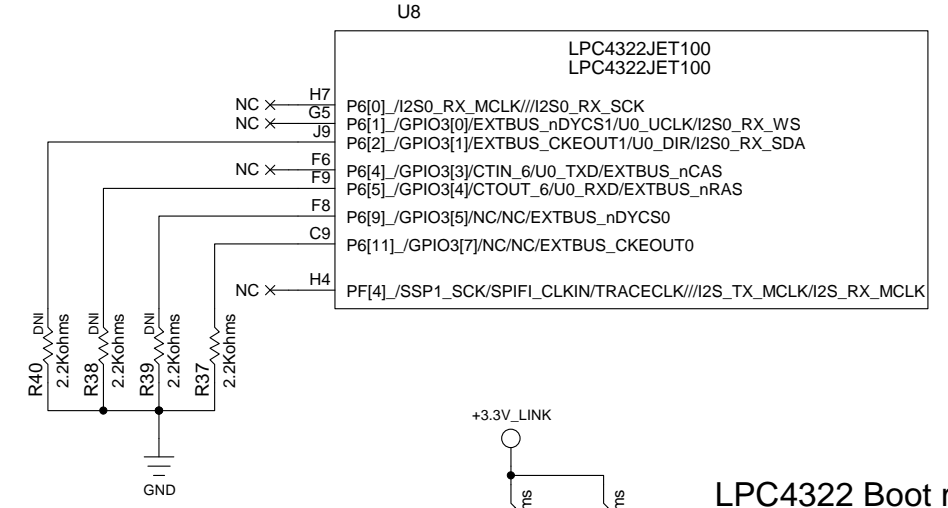
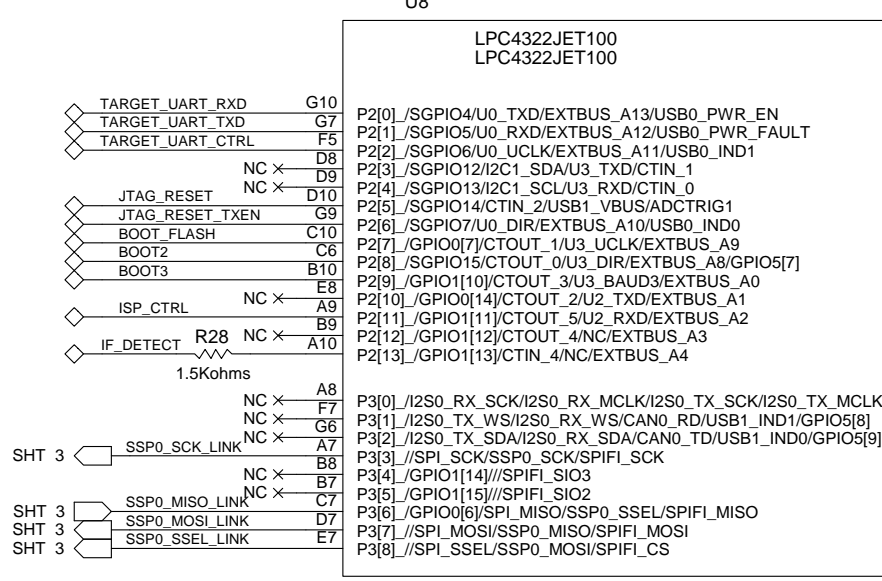
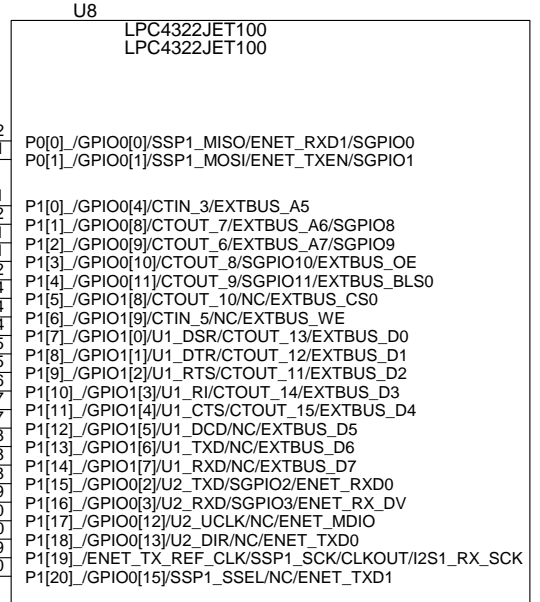
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
1	- Release	9/04/2014	
A	- 74LVC1G53 (U7) VDD changed to +3.3V; sht 3 - Add C60 47pF; sht 3 - Target P0_29 & P0_30 access now at J1; sht 6	9/07/2014	



Niobe VDD voltage select (JPx)
 VDD = 3.3V; 2 - 3 (default)
 VDD = 1.8V; 1 - 2

CONTRACT NO.		LINK2 LPC432x	
APPROVALS	DATE	NXP Semiconductors	
DRAWN	d.consiglio	411 E. Plumeria Dr	
CHECKED		San Jose, CA 95134	
ISSUED	9/07/14	www.standardics.nxp.com/microcontrollers/	
SIZE	FSCM NO.	DWG. NO.	REV
D		LPC54102 LQFP64	A
SCALE		SHEET	1 OF 06

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

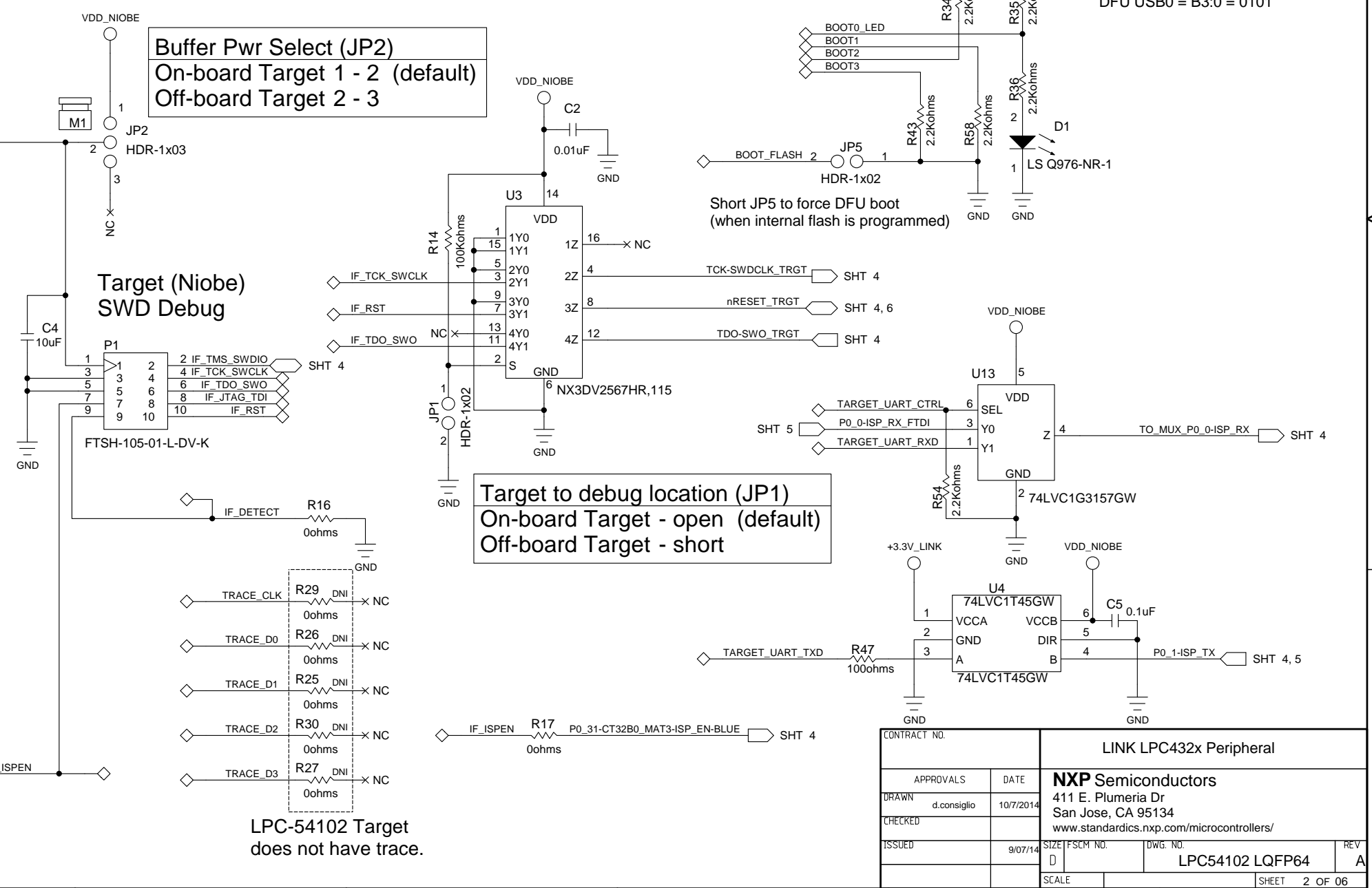
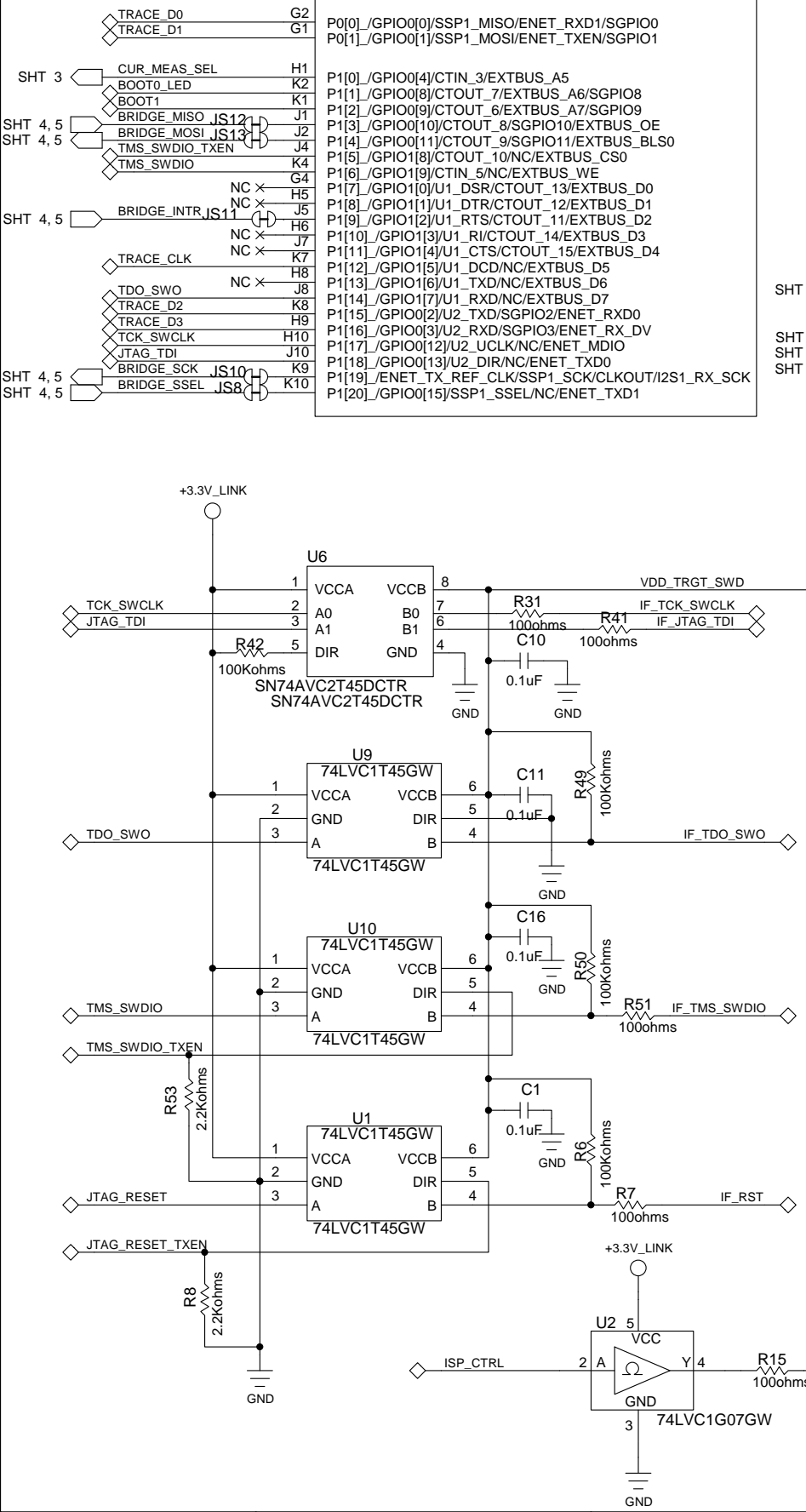


LPC4322 Boot mode
DFU USB0 = B3:0 = 0101

Buffer Pwr Select (JP2)
On-board Target 1 - 2 (default)
Off-board Target 2 - 3

Target to debug location (JP1)
On-board Target - open (default)
Off-board Target - short

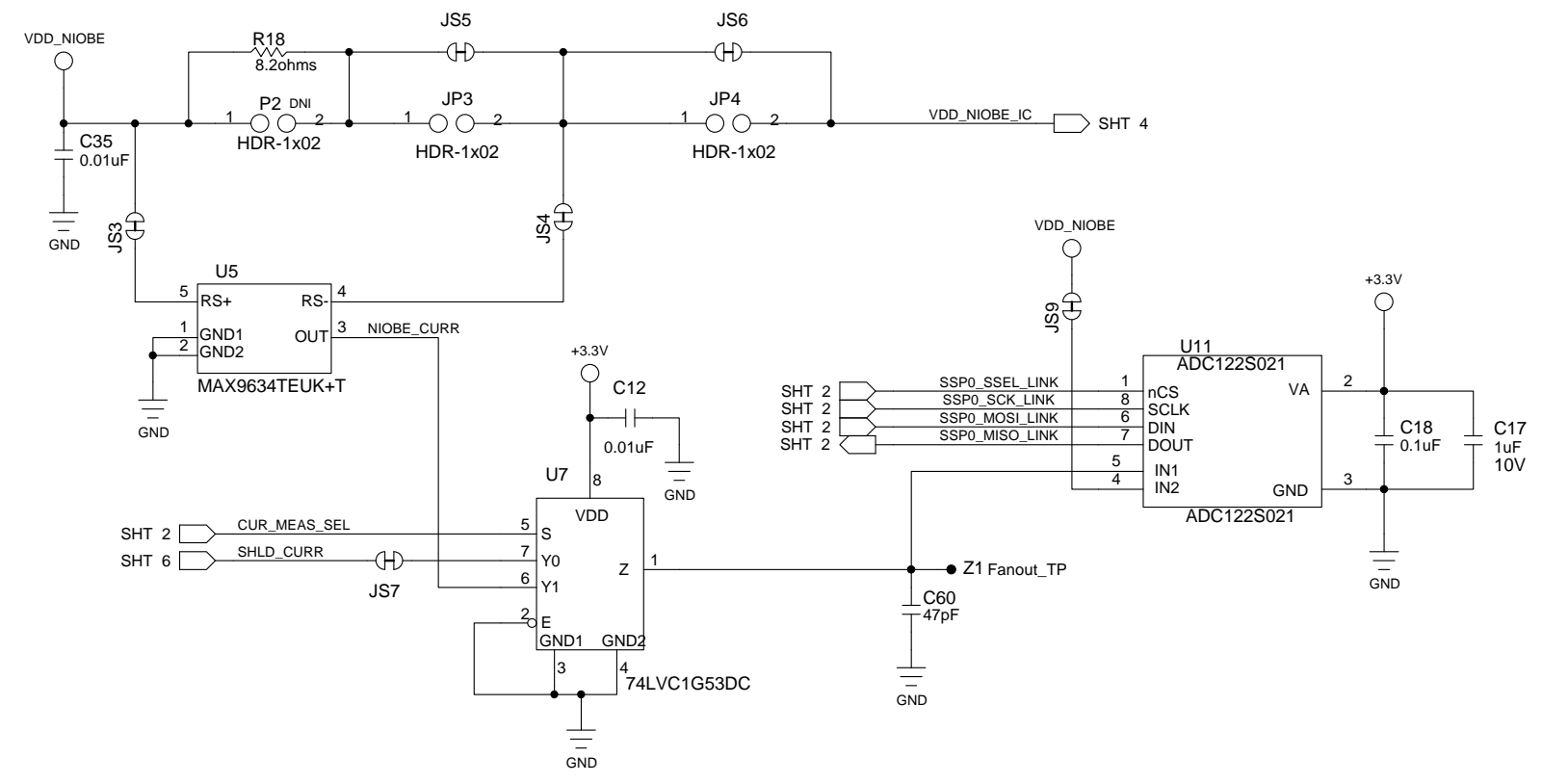
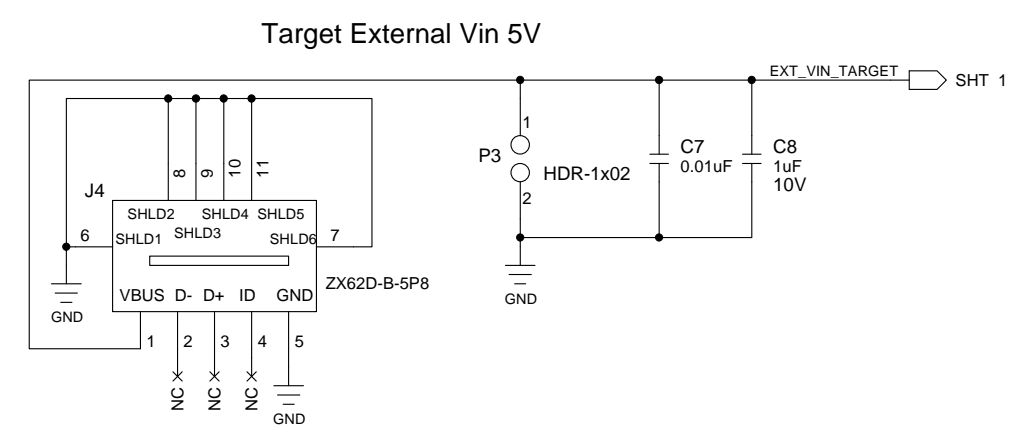
Target (Niobe) SWD Debug



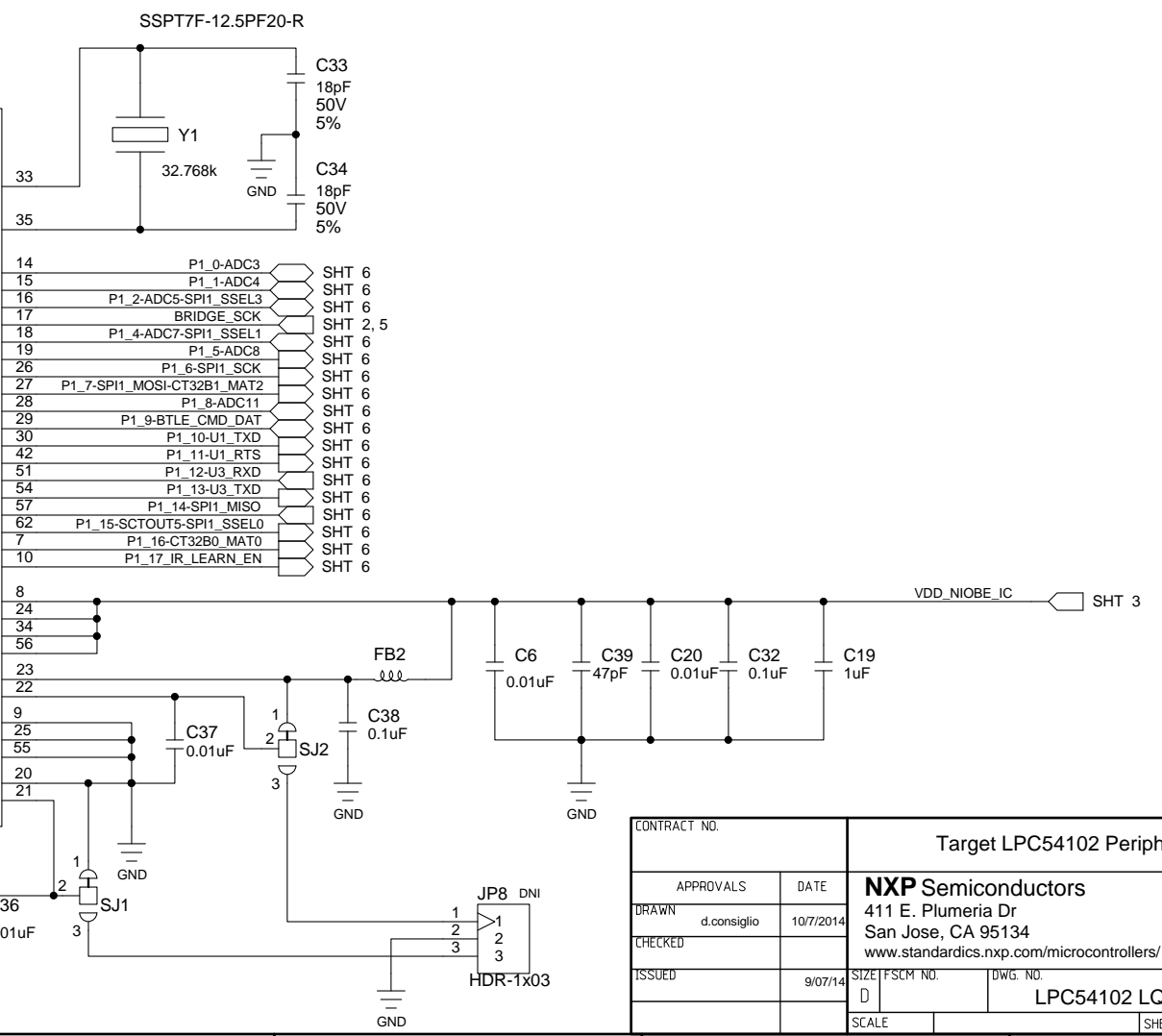
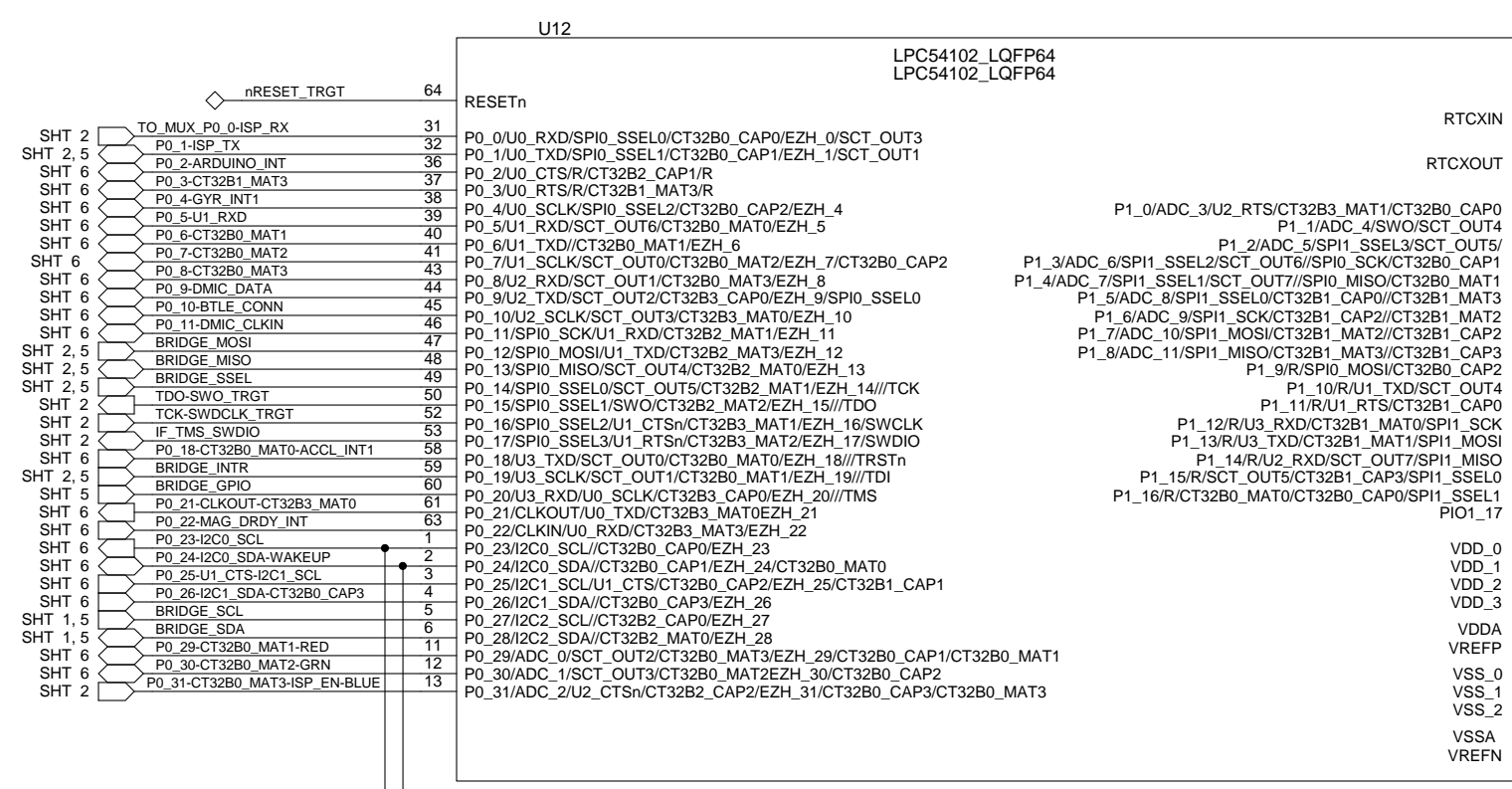
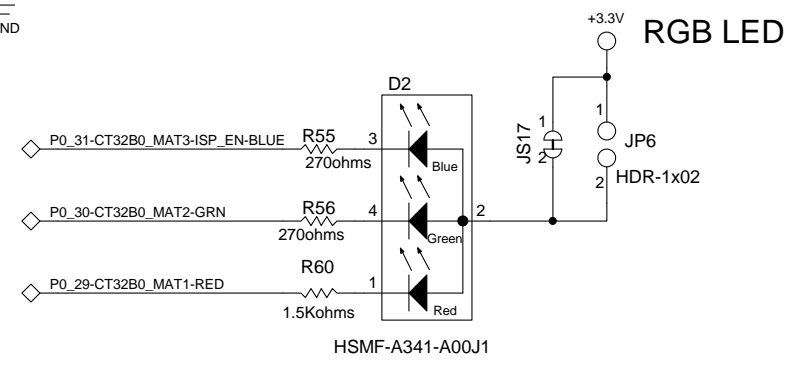
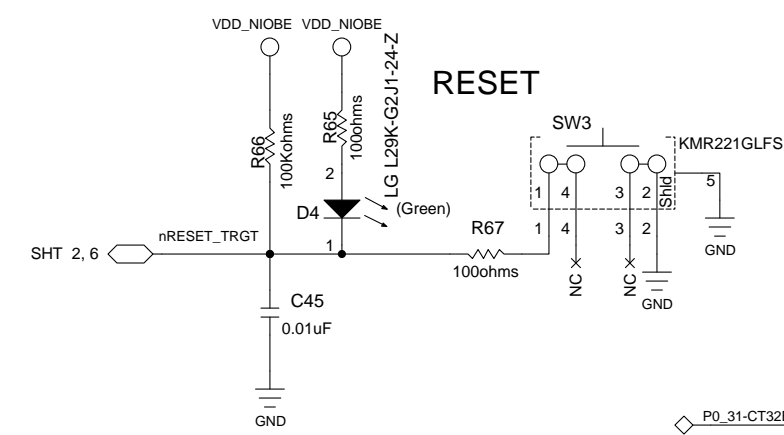
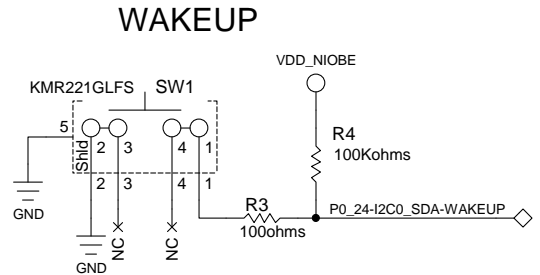
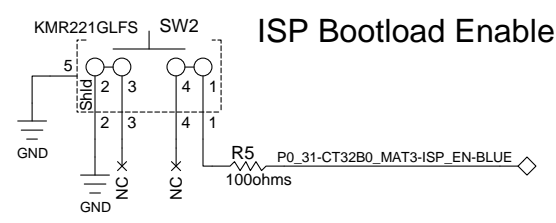
LPC-54102 Target does not have trace.

CONTRACT NO.		LINK LPC432x Peripheral	
APPROVALS	DATE	NXP Semiconductors	
DRAWN d.consiglio	10/7/2014	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
CHECKED		SIZE	FSCM NO.
ISSUED	9/07/14	DWG. NO.	REV
		LPC54102 LQFP64	A
SCALE		SHEET	2 OF 06

LPC54102			ADC111S021 12bit ADC
Vsense	LPC54102	maximum	ADC input
8.2 ohm	Current 1-lsb	current	1- lsb
32uV	3.9uA	16mA	800uV



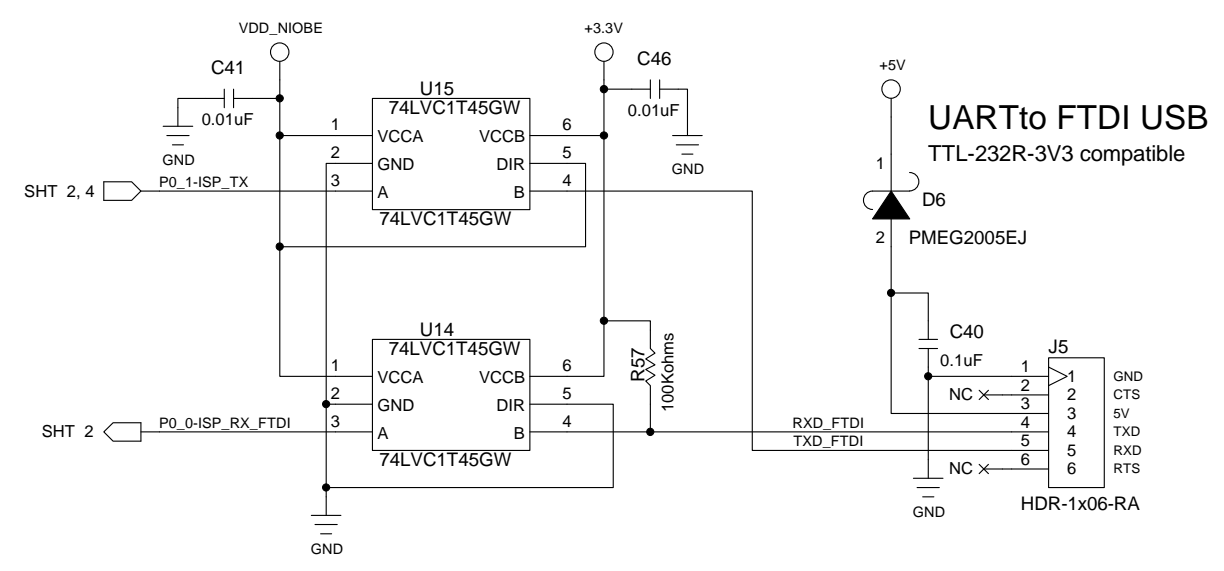
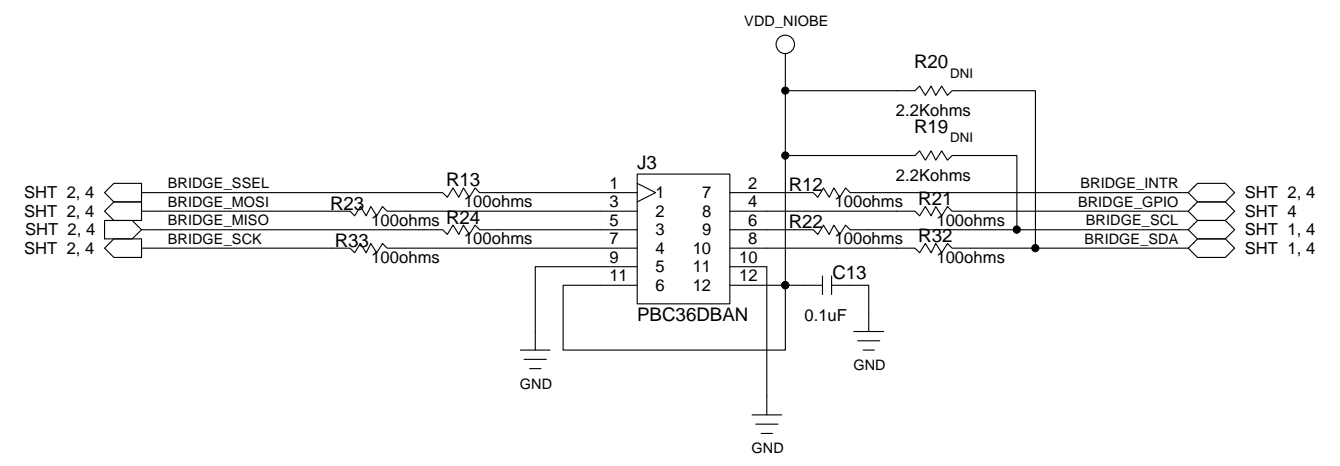
CONTRACT NO.		Target LPC54102 Power				
APPROVALS	DATE	NXP Semiconductors 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/				
DRAWN	d.consiglio					10/7/2014
CHECKED						
ISSUED	9/07/14	SIZE	FSCM NO.	DWG. NO.	REV	
		D		LPC54102 LQFP64	A	
		SCALE		SHEET	3 OF 06	



CONTRACT NO.		Target LPC54102 Peripheral	
APPROVALS	DATE	NXP Semiconductors 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/	
DRAWN	10/7/2014		
CHECKED			
ISSUED	9/07/14		
SIZE	FSCM NO.	DWG. NO.	REV.
D		LPC54102 LQFP64	A
SCALE		SHEET	4 OF 06

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

SPI / I2C brdge header (PMOD compatible)



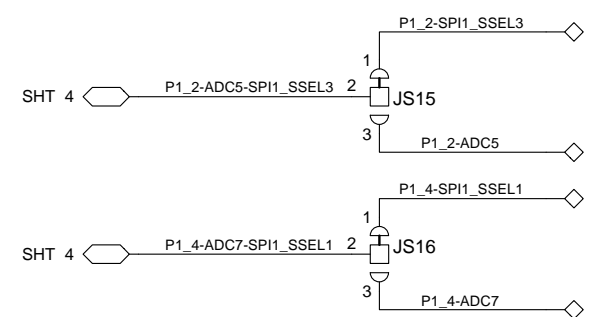
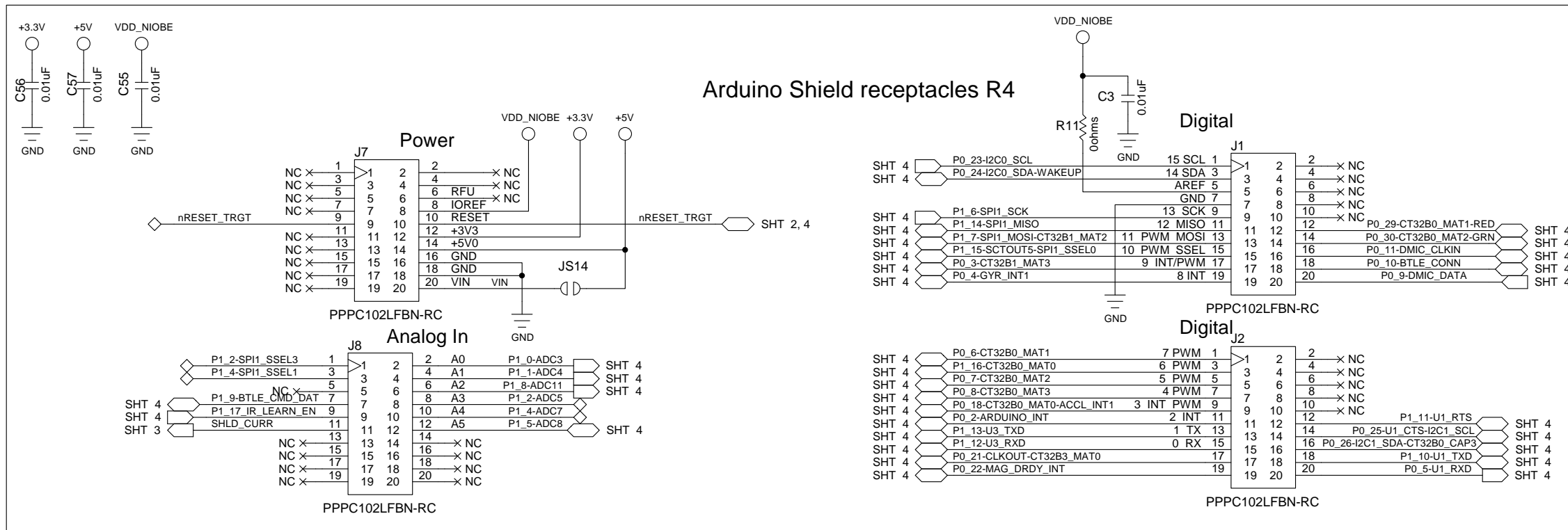
UART to FTDI USB

TTL-232R-3V3 compatible

CONTRACT NO.		I2C_SPI bridge hdr / FTDI			
APPROVALS	DATE	NXP Semiconductors 411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/			
DRAWN	d.consiglio				
CHECKED					
ISSUED	9/07/14	SIZE	FSCM NO.	DWG. NO.	REV
		D		LPC54102 LQFP64	A
SCALE				SHEET 5 OF 06	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

Arduino Shield receptacles R4



CONTRACT NO.		Arduino Interface			
APPROVALS	DATE	NXP Semiconductors			
DRAWN d.consiglio	10/7/2014	411 E. Plumeria Dr San Jose, CA 95134 www.standardics.nxp.com/microcontrollers/			
CHECKED		SIZE D	FSCM NO.	DWG. NO. LPC54102 LQFP64	REV A
ISSUED	9/07/14	SCALE		SHEET 6 OF 06	