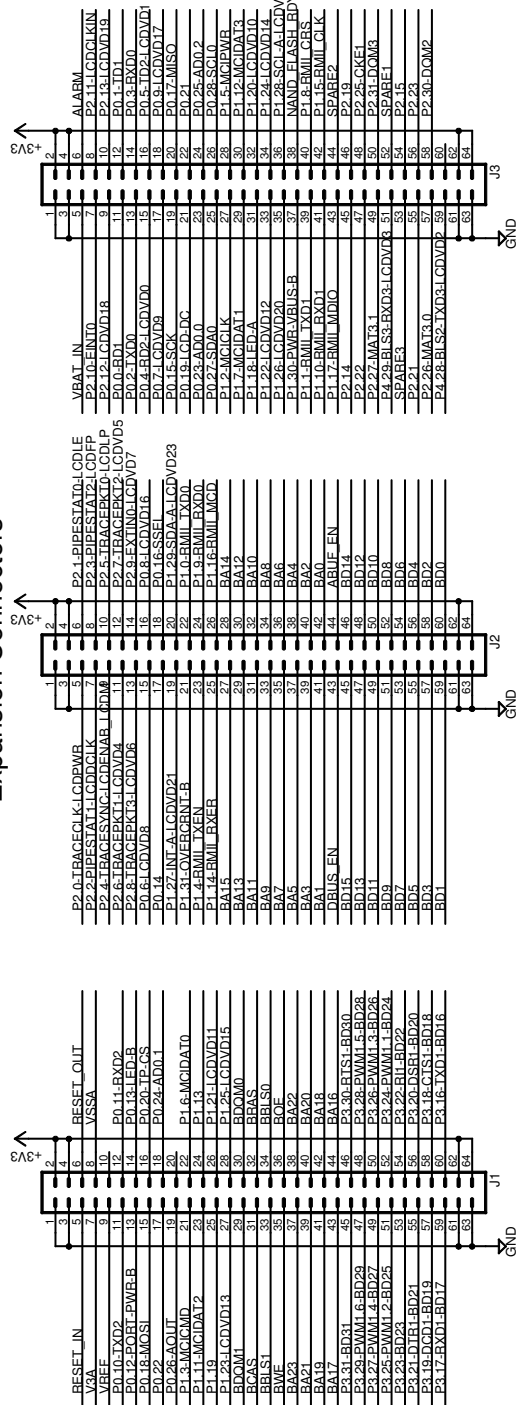
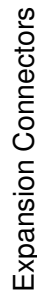
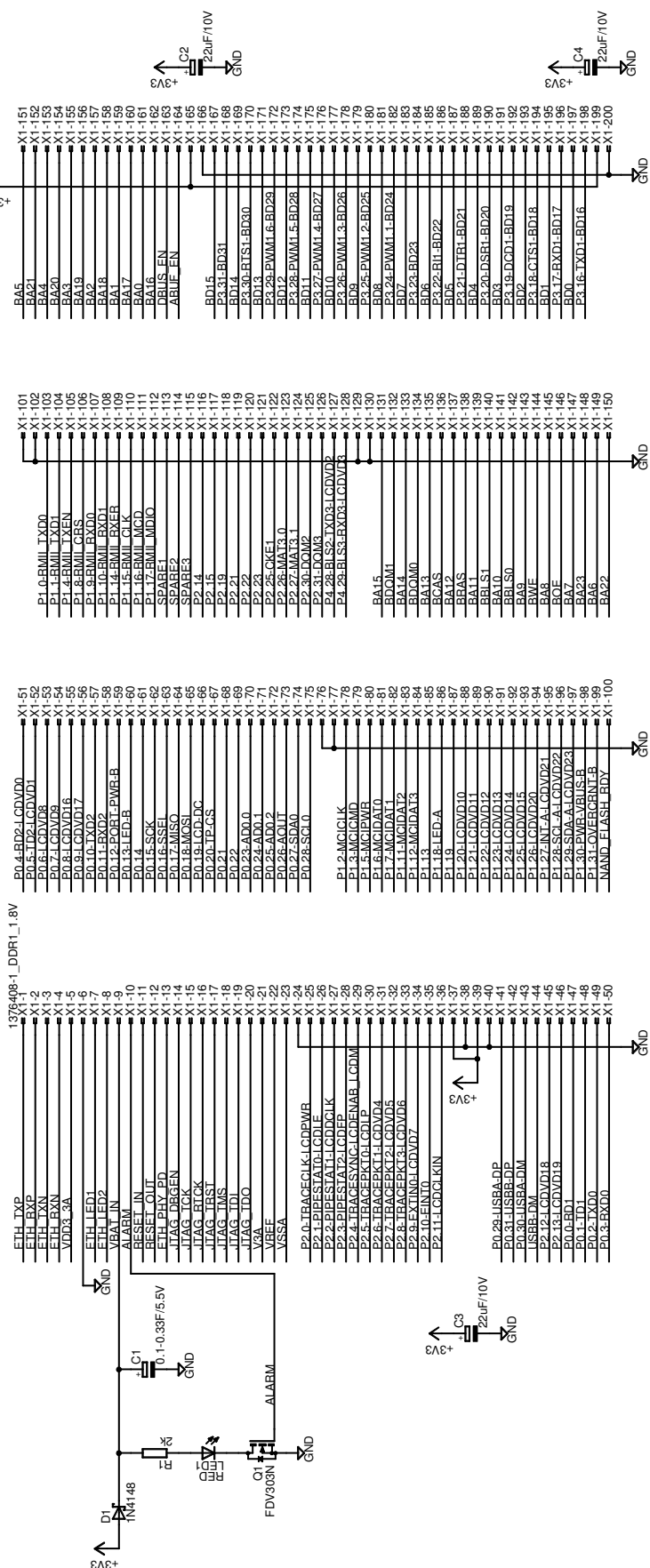
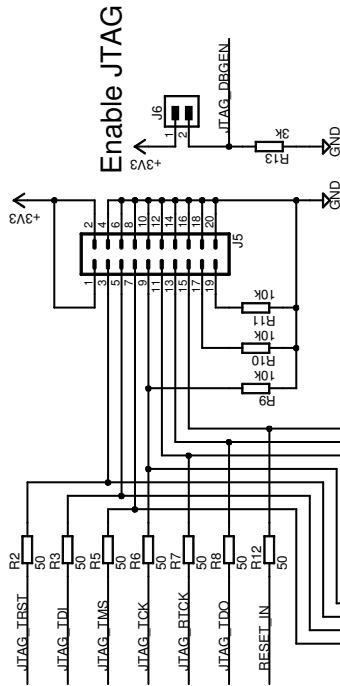


OEM Board



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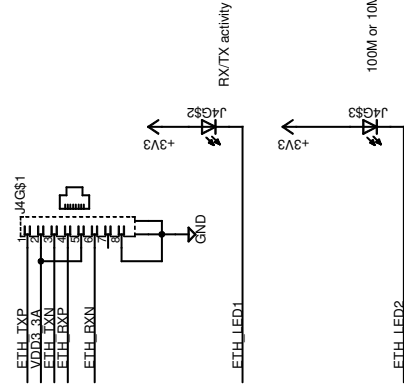
JTAG Interface



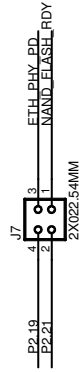
Reset Push-button & LED



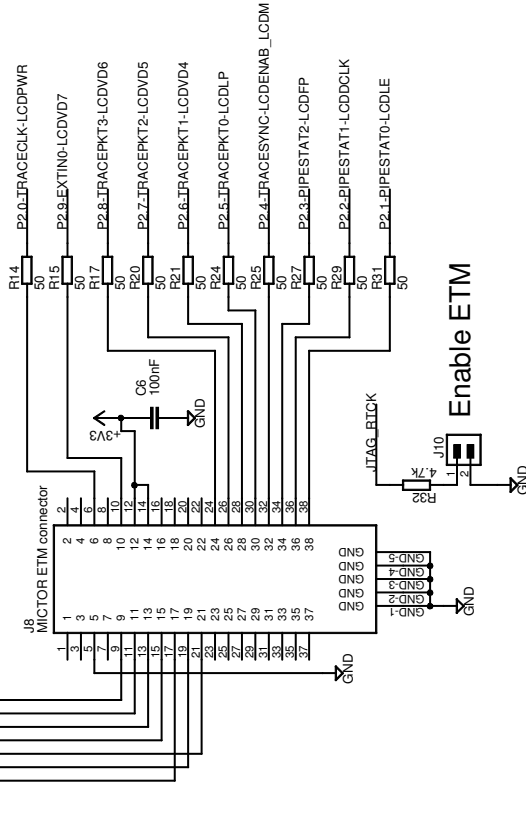
RJ45 Ethernet Connector



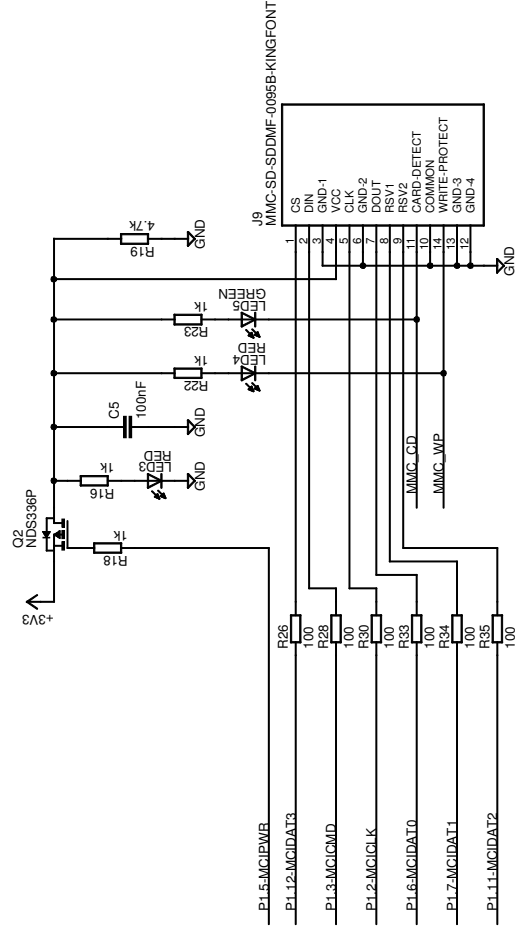
Optional Connections



ETM Interface

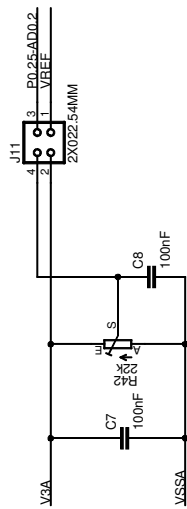


MMC/SD Memory Card I/F

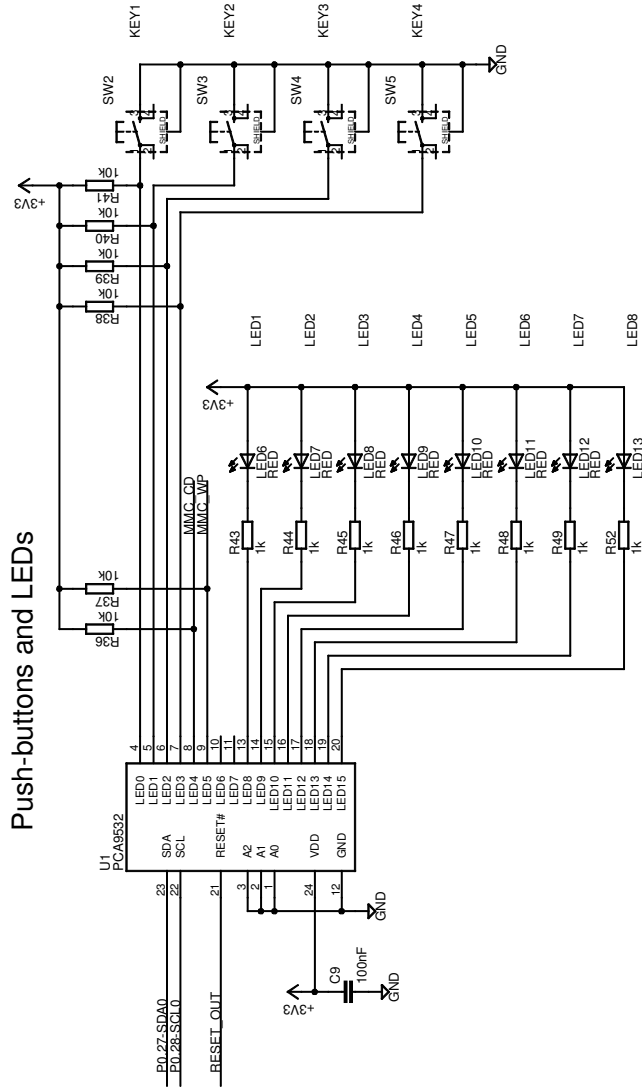


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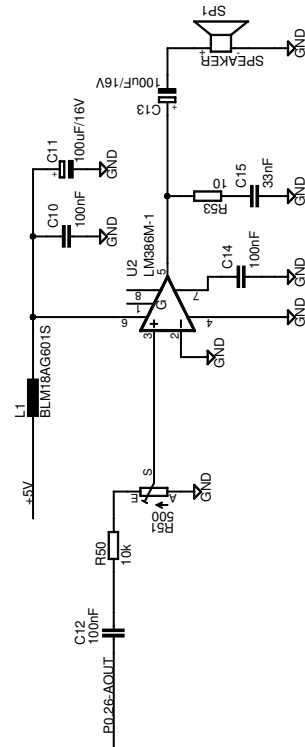
Analog Input



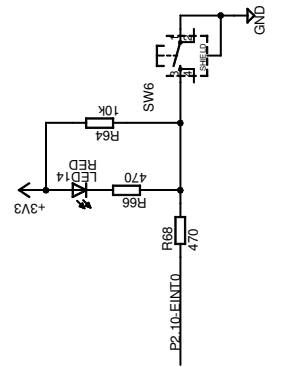
Push-buttons and LEDs



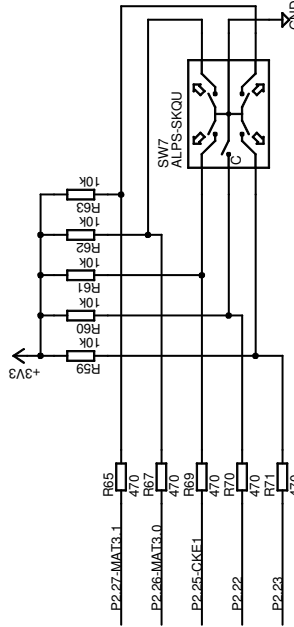
Speaker Amplifier (DAC output)



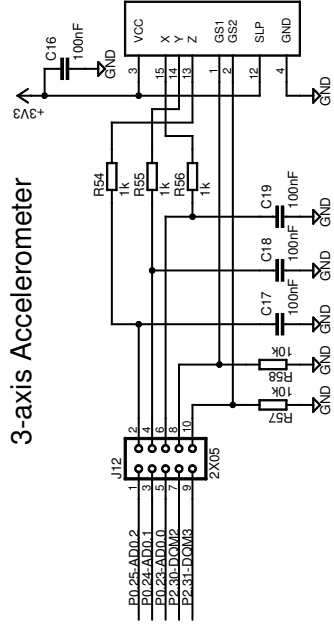
Interrupt (P2.10) Key



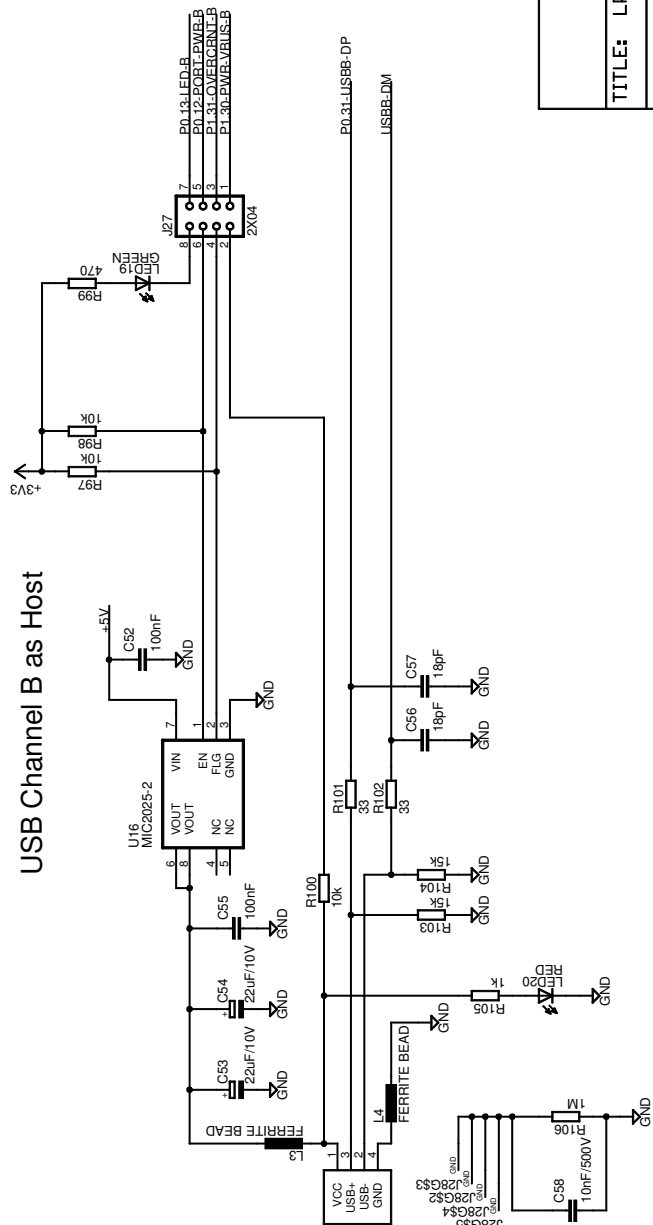
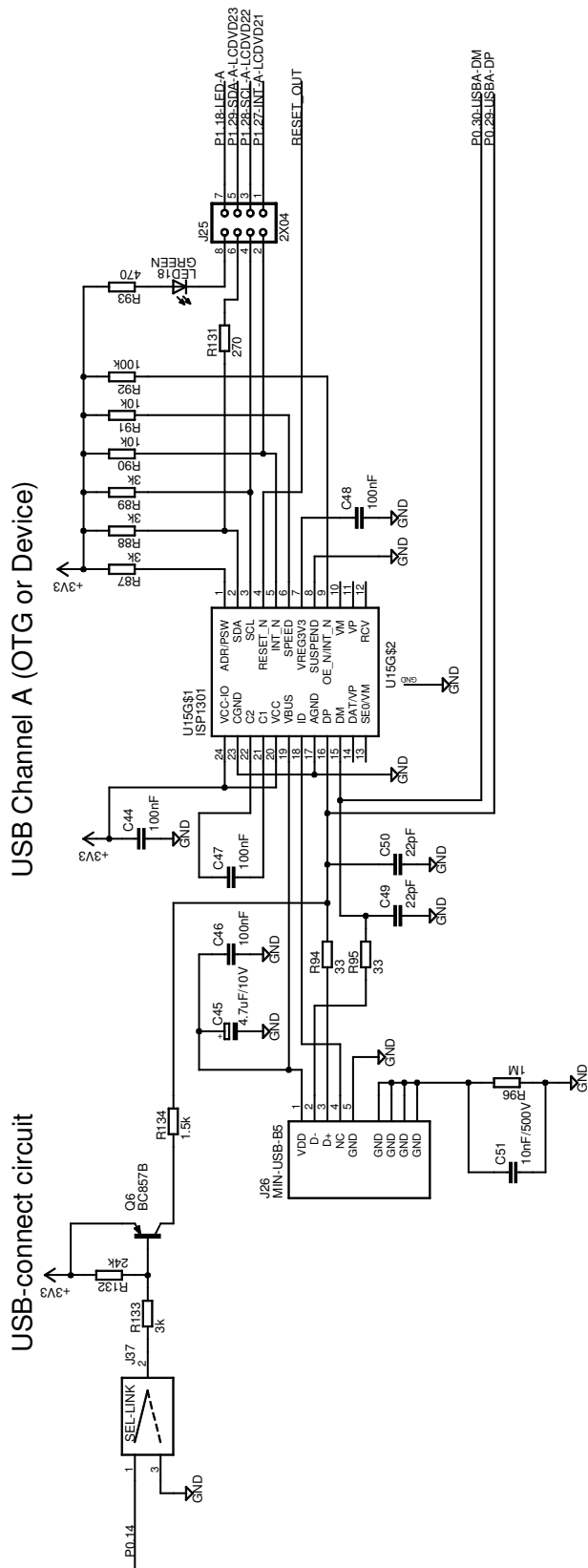
5-key Joystick Switch



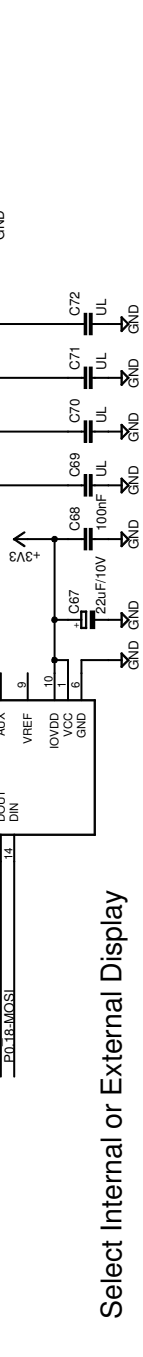
3-axis Accelerometer



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[illegible]

The timing diagram illustrates the relationship between input and output signals for two components, J34 and J35. For J34, the input signal P0.17_MISO transitions from high to low, and the output signal EXT_MISO transitions from high to low after a delay. For J35, the input signal P2.15 transitions from high to low, and the output signal EXT_JRQ transitions from high to low after a delay. The diagram uses solid lines for input signals and dashed lines for output signals, with arrows indicating the direction of signal flow.

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LCD Expansion Connector

