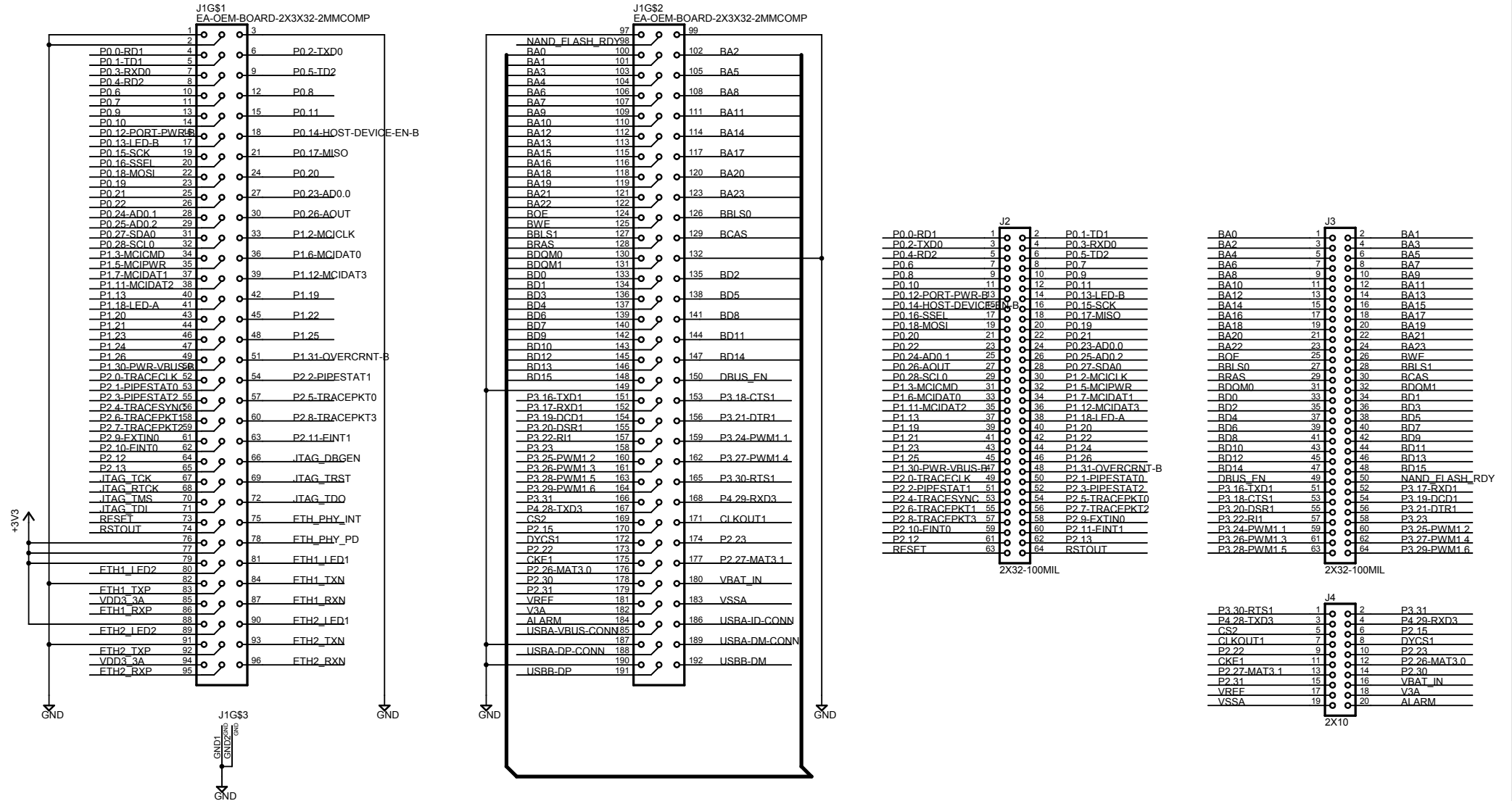


OEM Board



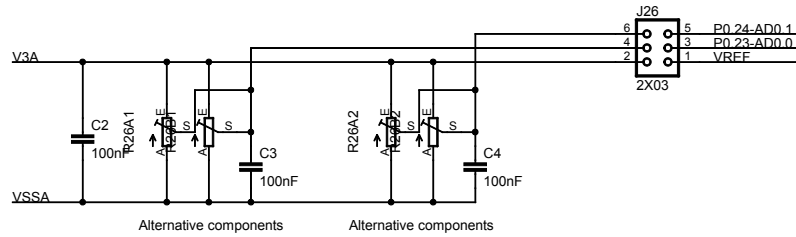
The image contains three circuit diagrams for Ethernet PHY connections:

- Diagram 1 (FTH1 PHY):** Shows the FTH1 PHY with TXP, TXN, RXP, RXN, and LED1 signals. The TXP, TXN, RXP, and RXN signals are connected to a J7G\$1 connector. The LED1 signal is connected to a +3V3 supply through a 10k resistor (R2). The TXN and RXN signals are also connected to a +3V3 supply through a J7G\$2 connector.
- Diagram 2 (FTH2 PHY):** Shows the FTH2 PHY with TXP, TXN, RXP, RXN, and LED1 signals. The TXP, TXN, RXP, and RXN signals are connected to a J20G\$1 connector. The LED1 signal is connected to a +3V3 supply through a 10k resistor (R2). The TXN and RXN signals are also connected to a +3V3 supply through a J20G\$2 connector.
- Diagram 3 (P2 11-FINT1 PHY):** Shows the P2 11-FINT1 PHY with P0_20, FTH_PHY_INT, and FTH_PHY_PD signals. The P0_20 signal is connected to a J23 connector. The FTH_PHY_INT and FTH_PHY_PD signals are connected to a +3V3 supply through a 10k resistor (R4).

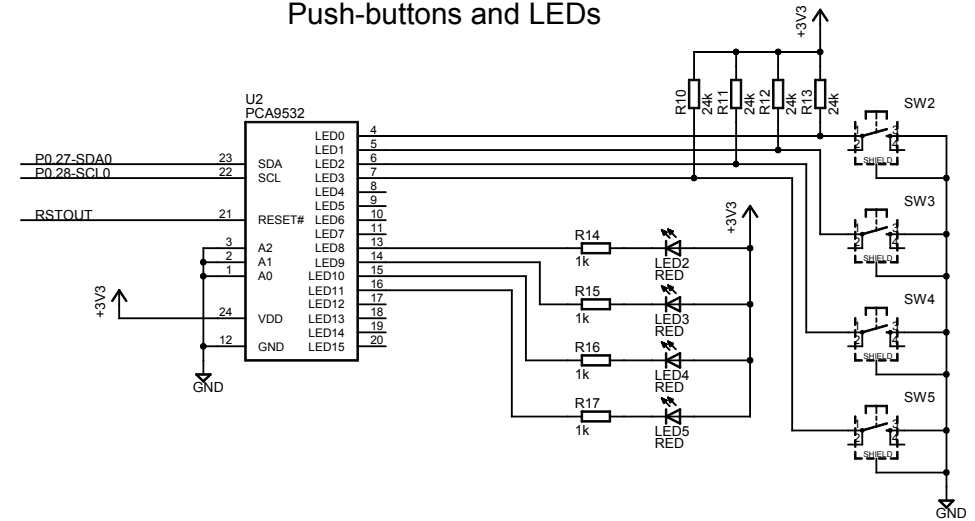
[illegible]

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Document Number:	REV:
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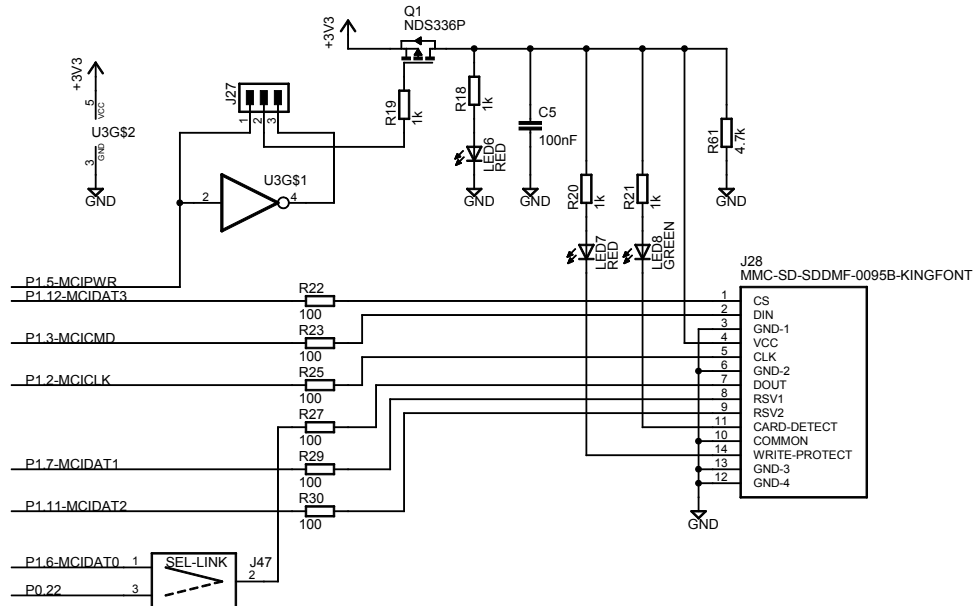
Analog inputs



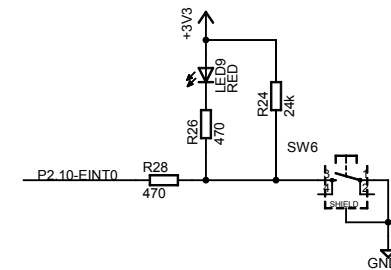
Push-buttons and LEDs



MMC/SD Memory Card I/F



Interrupt (P2.10) key



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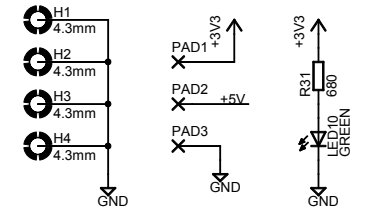
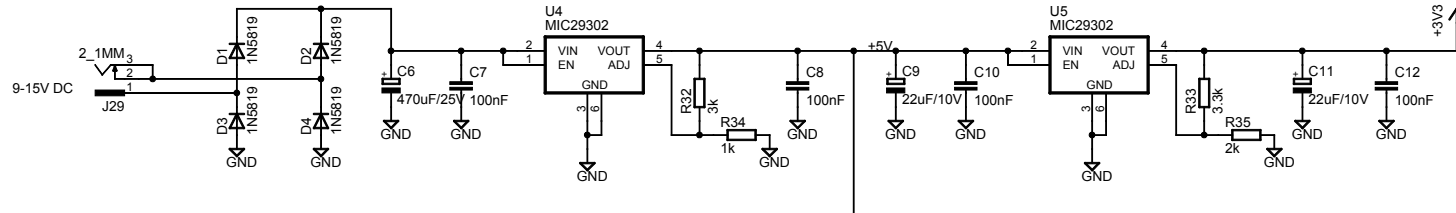
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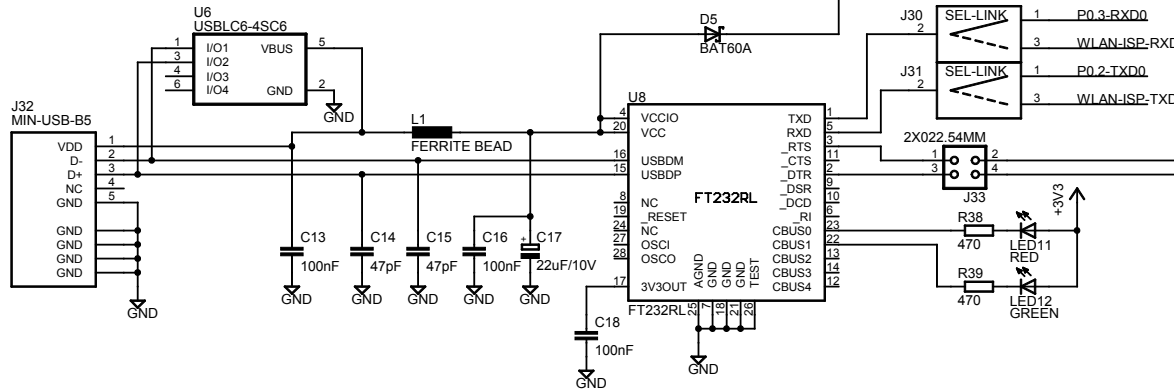
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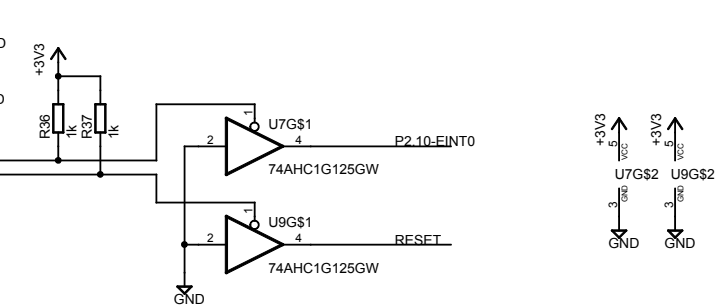
Power Supply



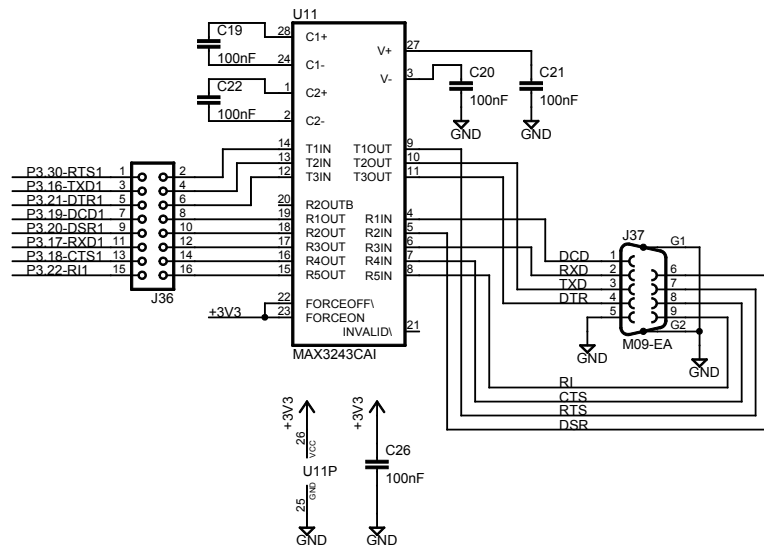
UART #0 over USB Serial Bridge



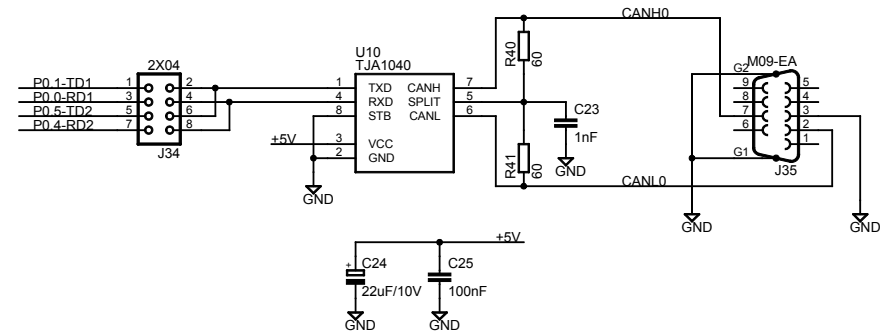
ISP functionality



UART #1 RS232 full modem



CAN transceiver



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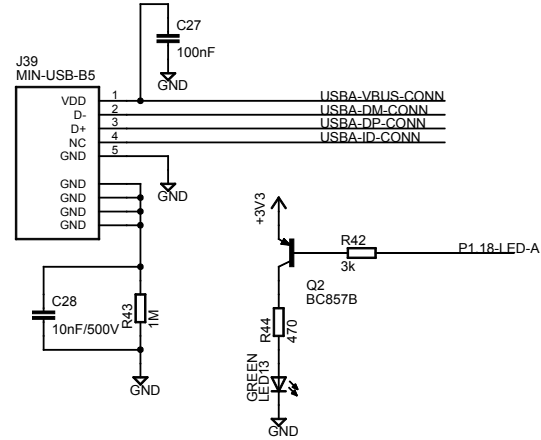
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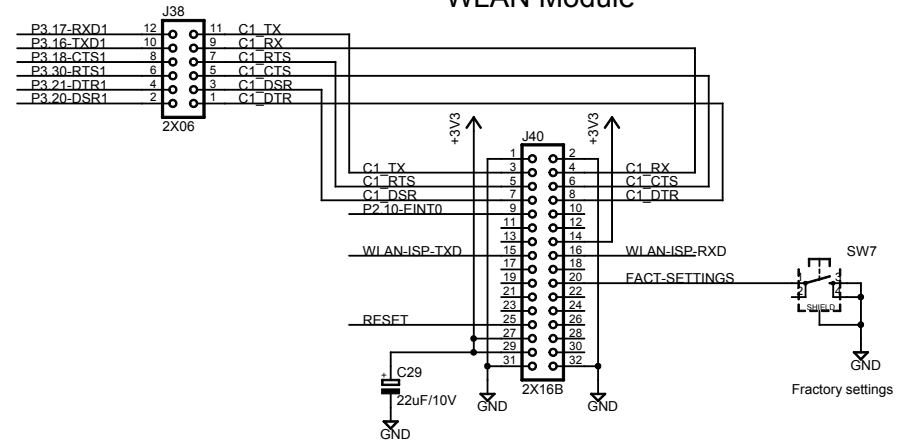
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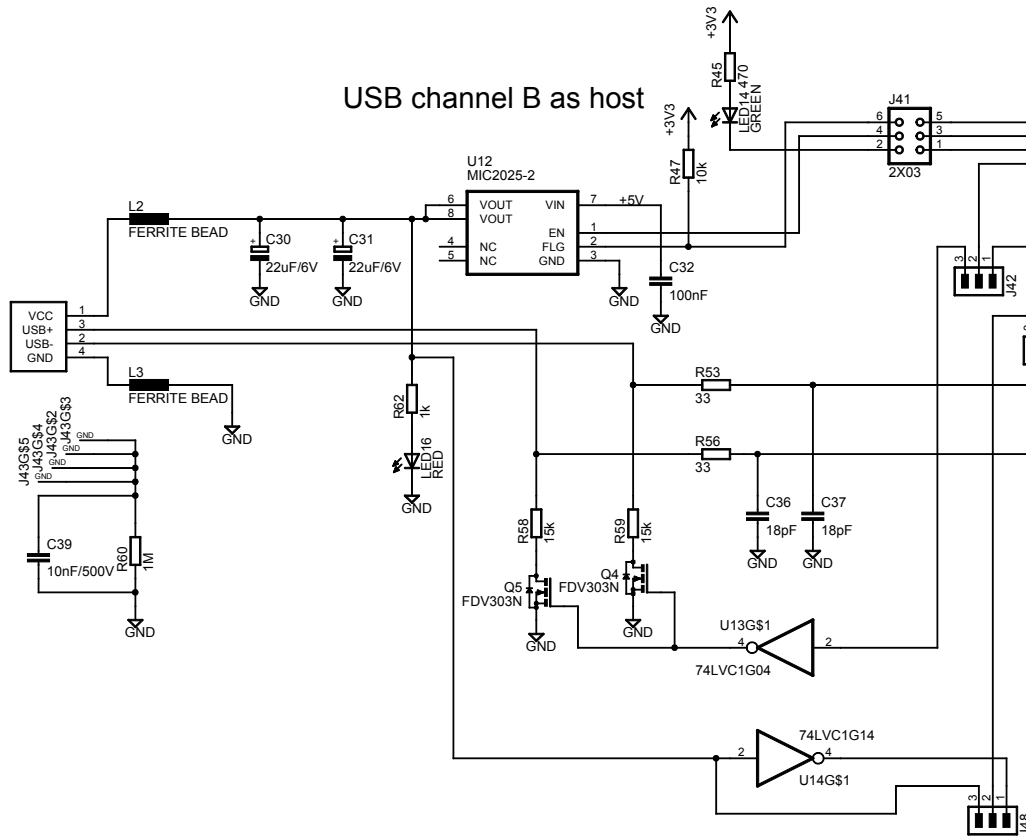
USB channel A (OTG)



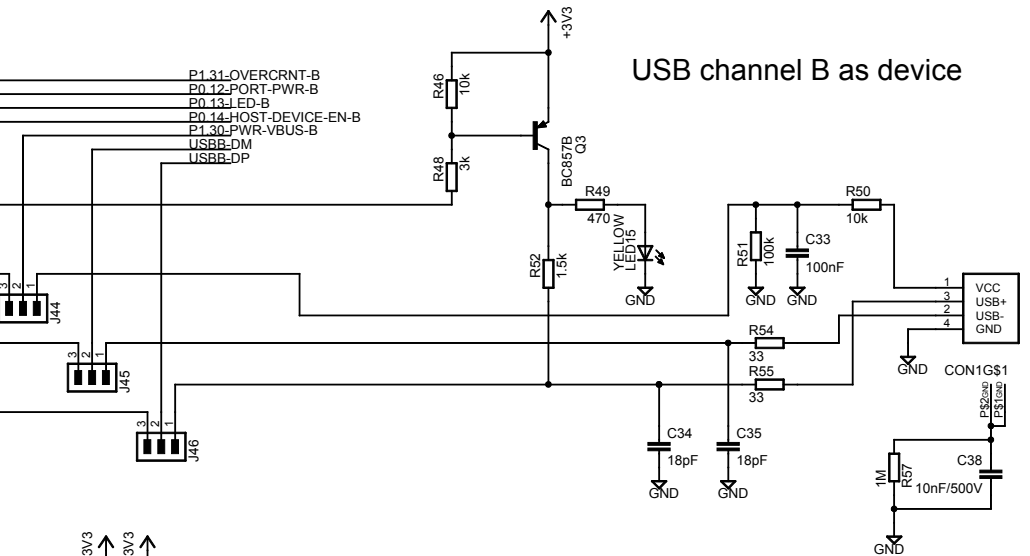
WLAN Module



USB channel B as host



USB channel B as device



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