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Rev 1.0

Original revision



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TITLE: LPC31xx Base Board v1.0

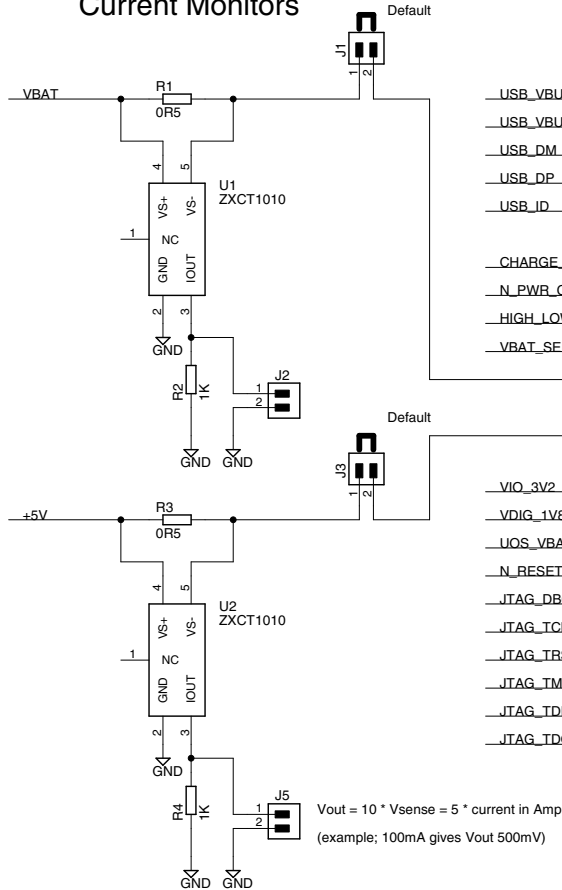
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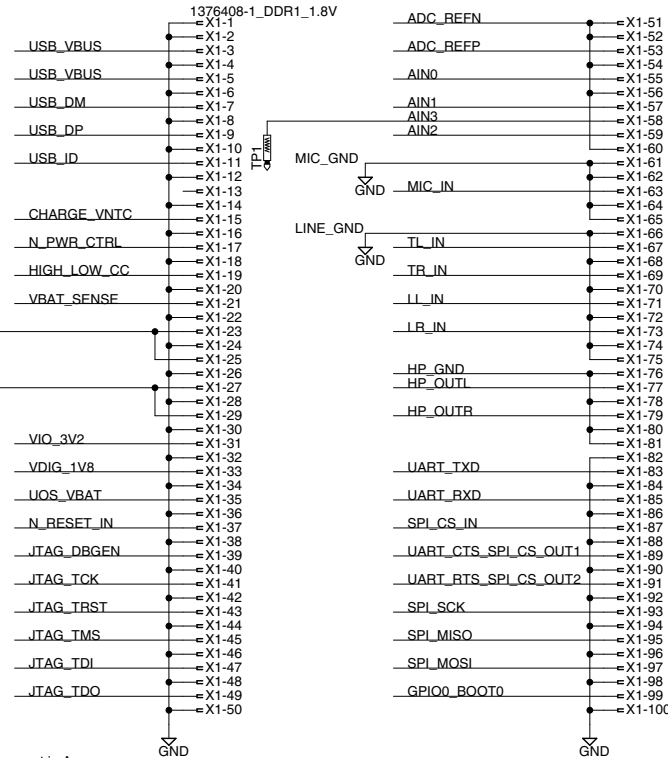
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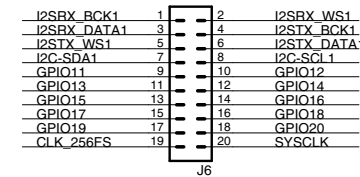
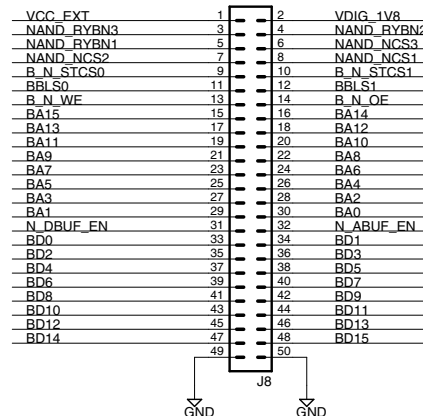
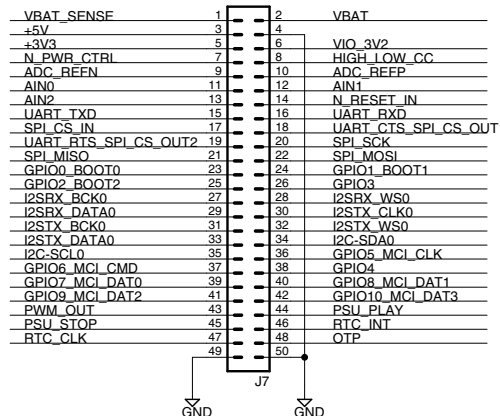
## Current Monitors



## LPC31xx OEM Board



## Expansion Connectors



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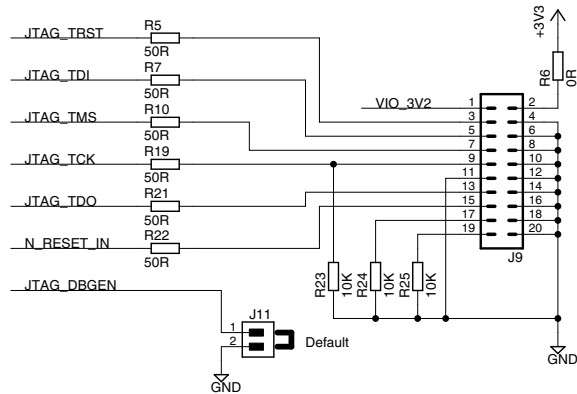
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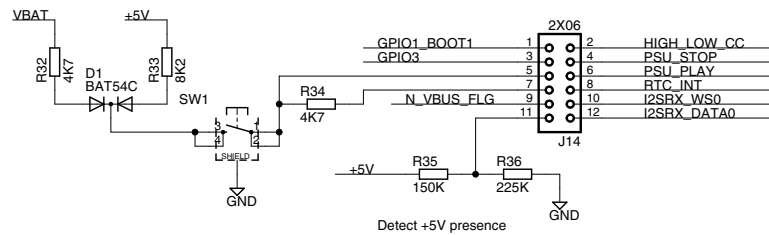
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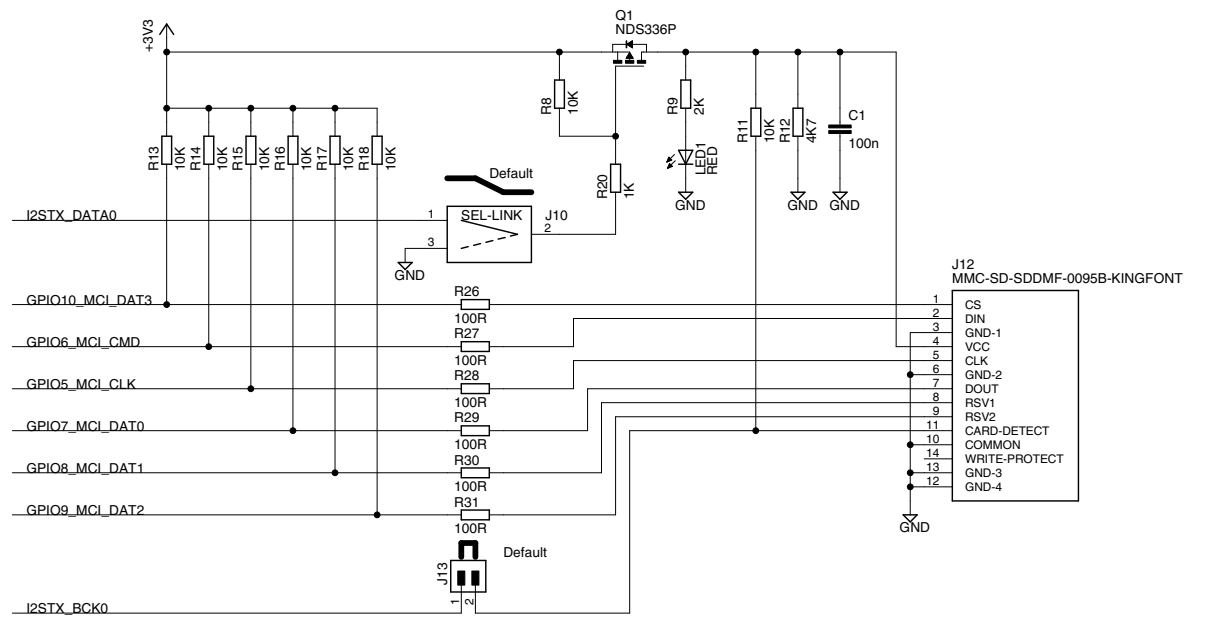
## JTAG Interface



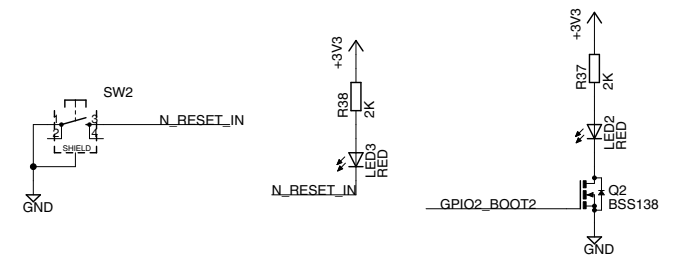
## Optional Connections



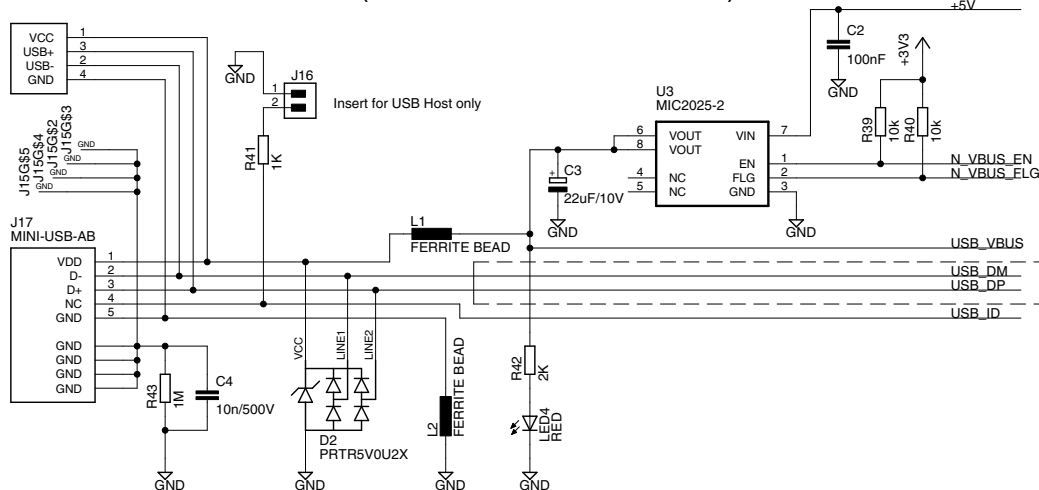
## MMC/SD Memory Card Interface



## Reset Push-button & LEDs



## USB Host/OTG Interface (with A & mini-AB connector)



45 ohm impedance routing, 90 ohm differential  
No tracks within 10 times trace width  
No via's and no sharp angles



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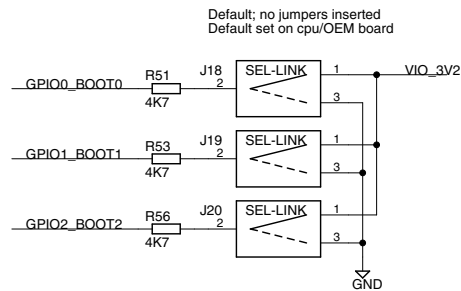
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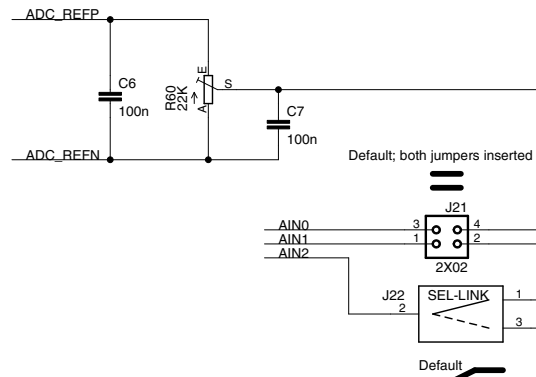
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## Boot mode

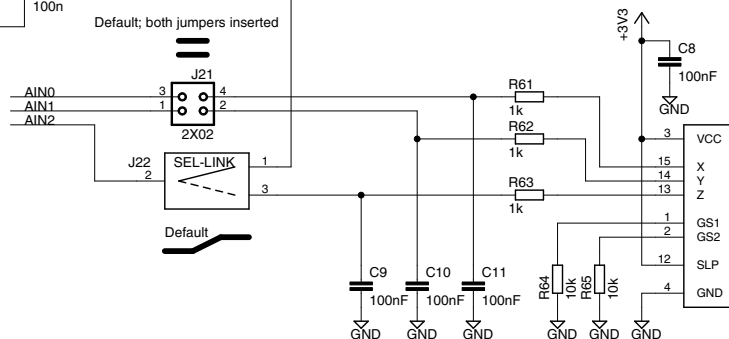
GPIO0	GPIO1	GPIO2	Boot source
LOW	LOW	LOW	NAND boot
LOW	LOW	HIGH	SPI boot
LOW	HIGH	LOW	USB via DPU class
LOW	HIGH	HIGH	MMC/SD card
HIGH	LOW	HIGH	Functional test mode
HIGH	HIGH	LOW	UART 115.2kbps 8N1
HIGH	HIGH	HIGH	Test mode



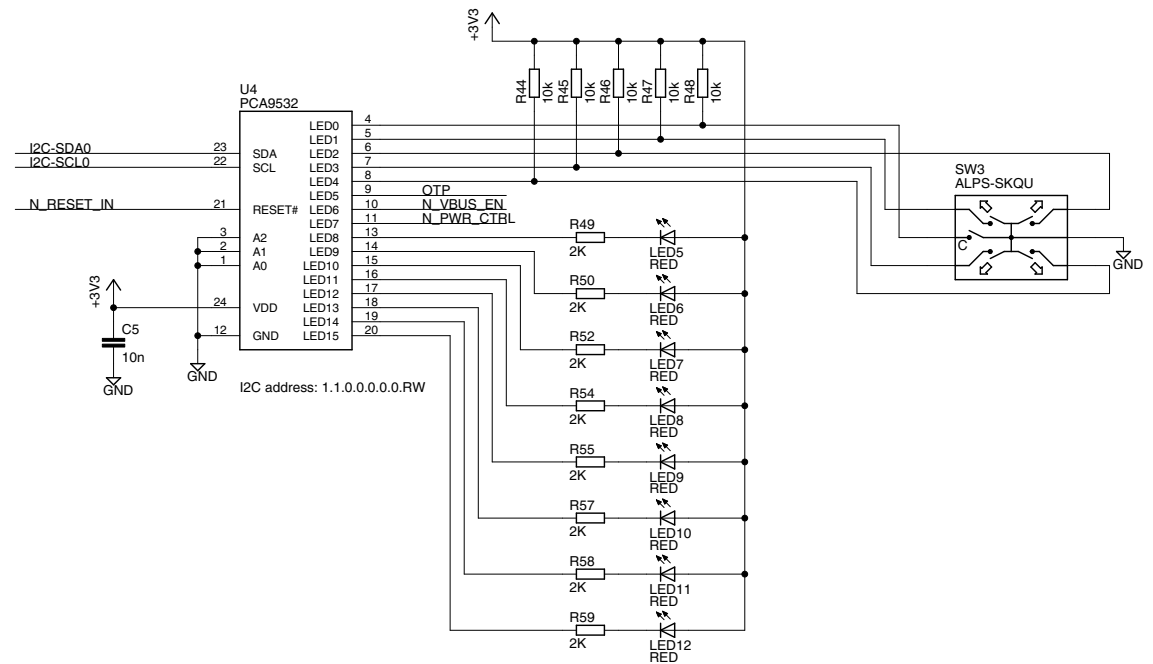
## Analog Input



## 3-axis accelerometer



## LEDs and 5-key joystick switch



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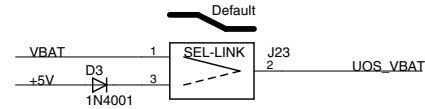
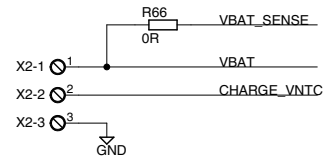
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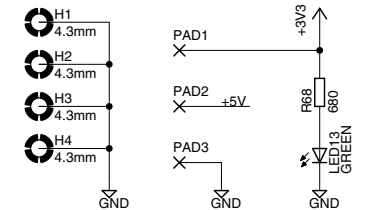
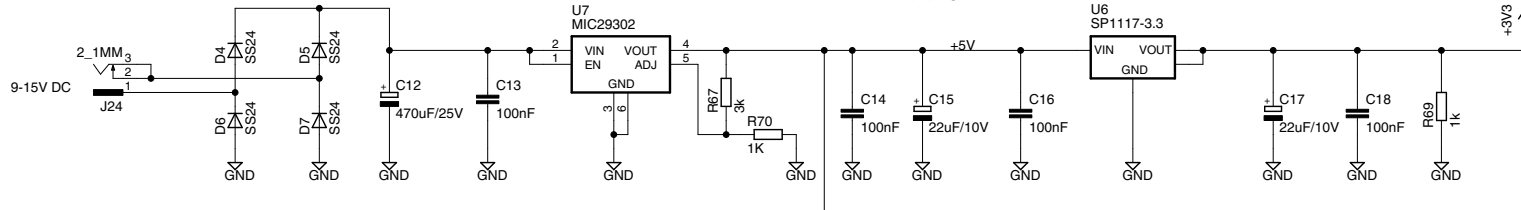
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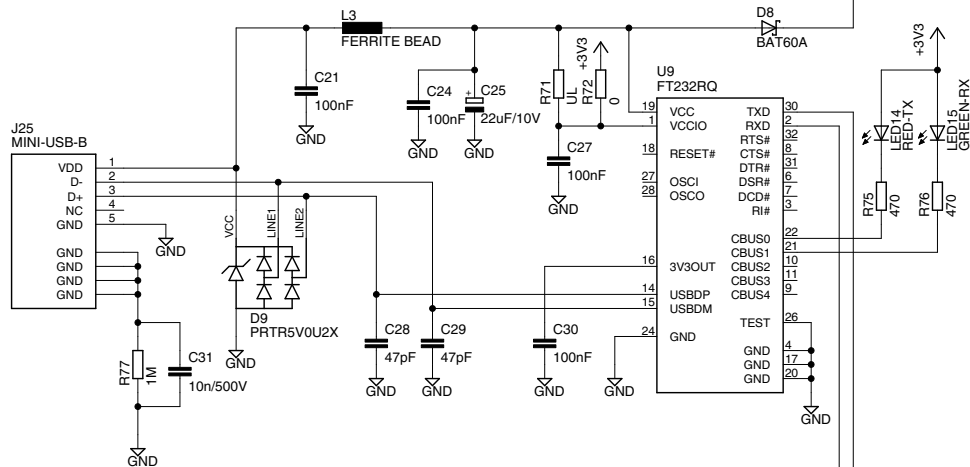
## Li-ion Battery



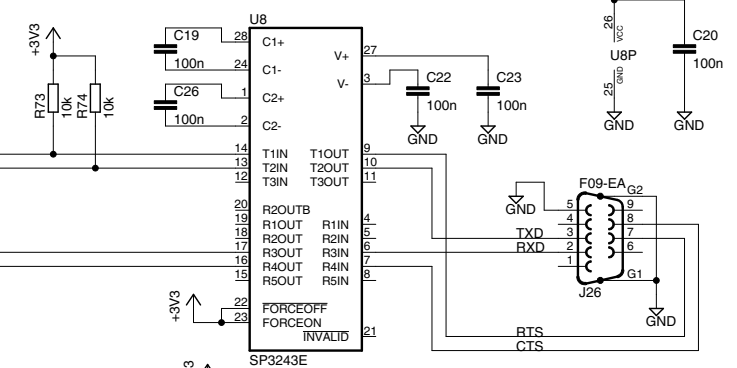
## Power Supply



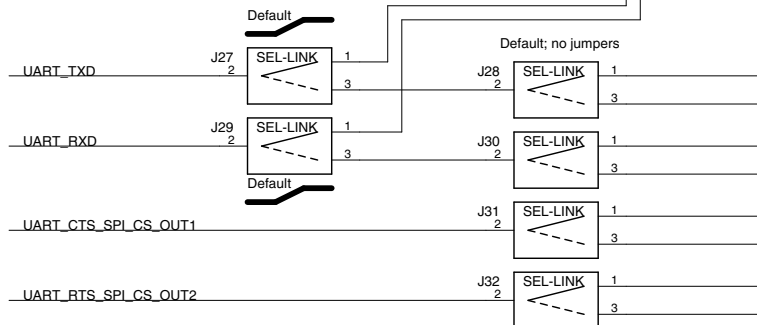
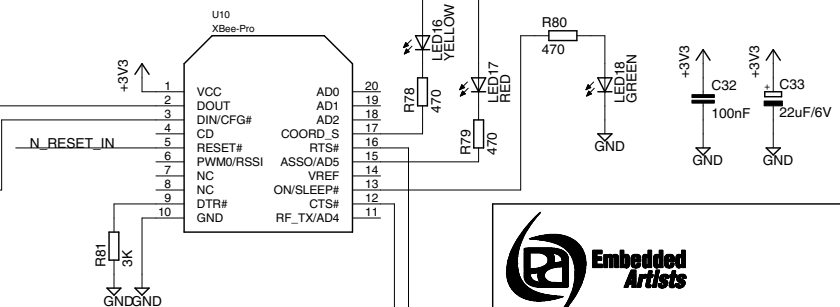
## UART over USB Serial Bridge



## UART RS232



## MaxStream XBee module



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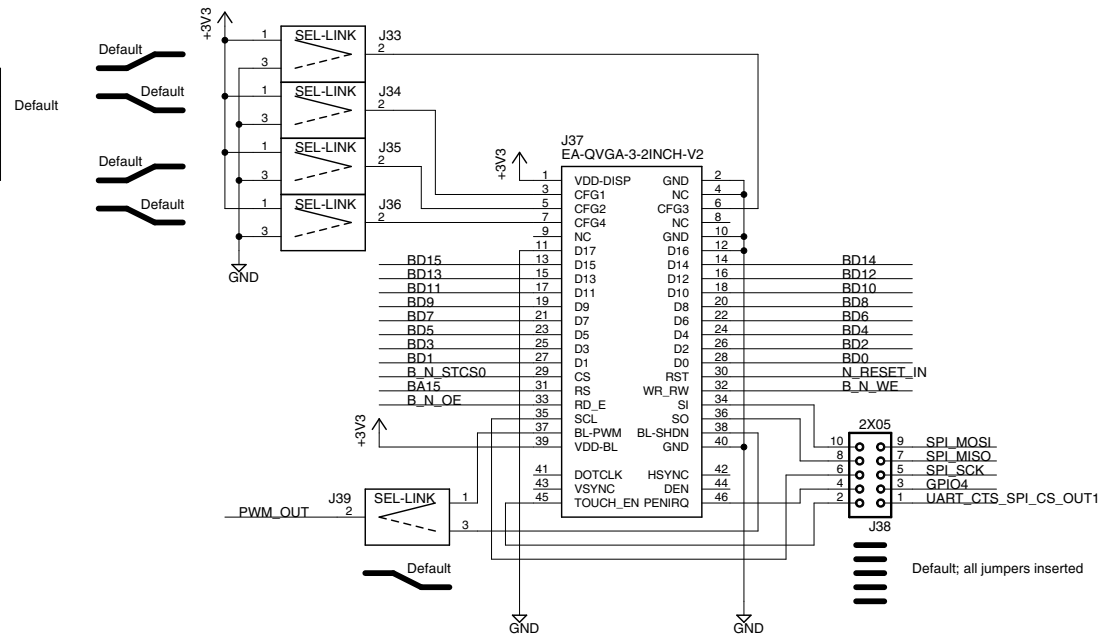
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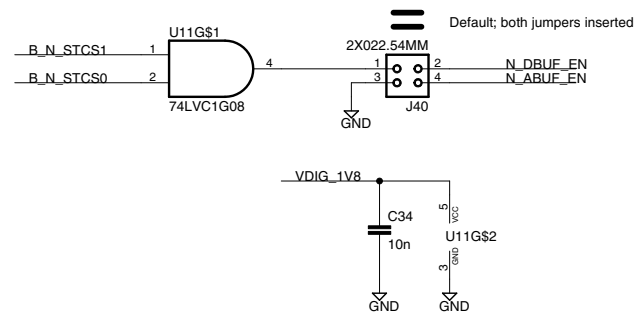
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## Interface Setting

CFG1	CFG2	CFG3	CFG4	Interface mode
HIGH	LOW	LOW	HIGH	16-bit parallel
LOW	LOW	HIGH	LOW	9-bit parallel
LOW	LOW	LOW	HIGH	8-bit parallel



## Enable Buffers



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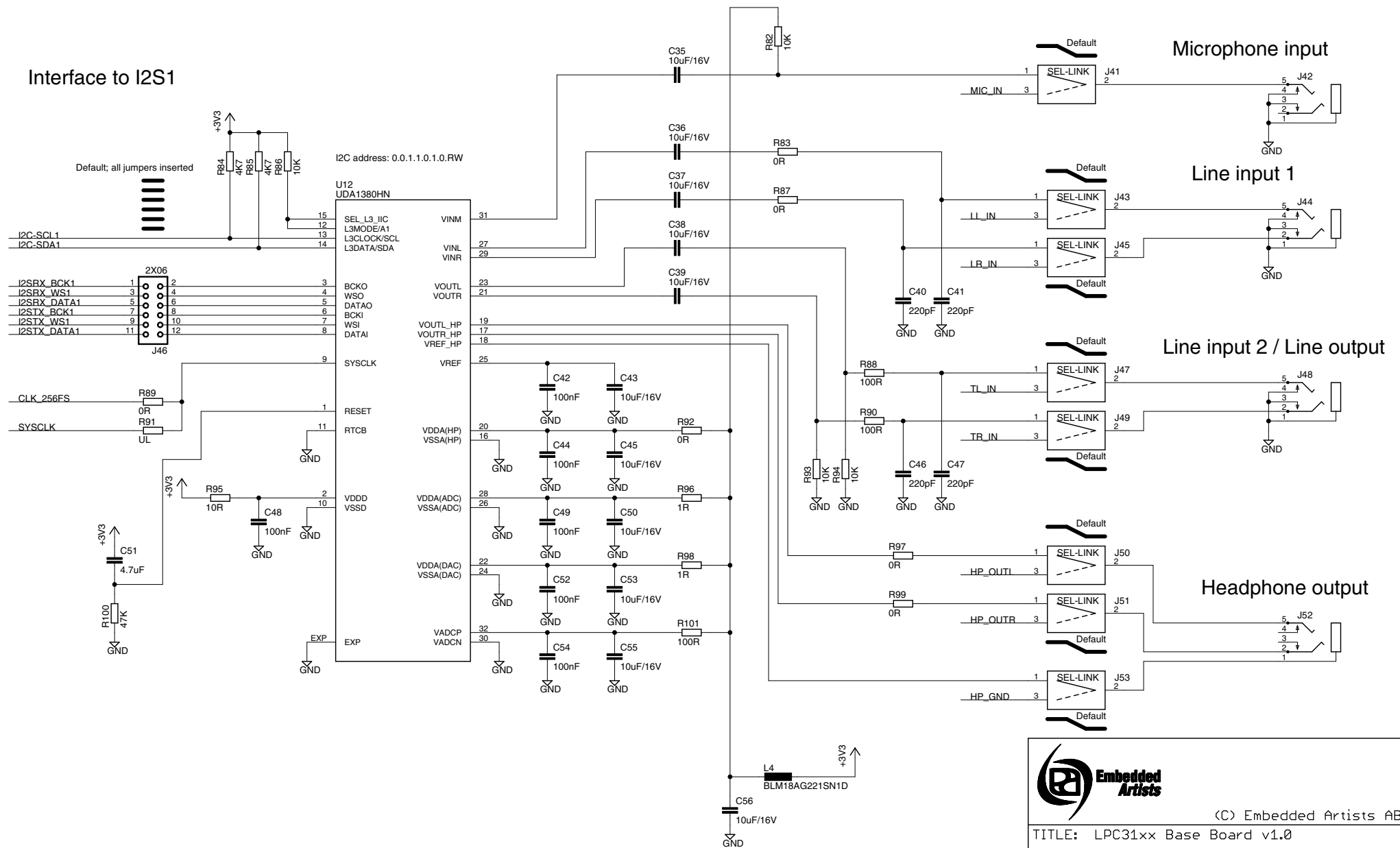
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# Stereo Audio Codec



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