


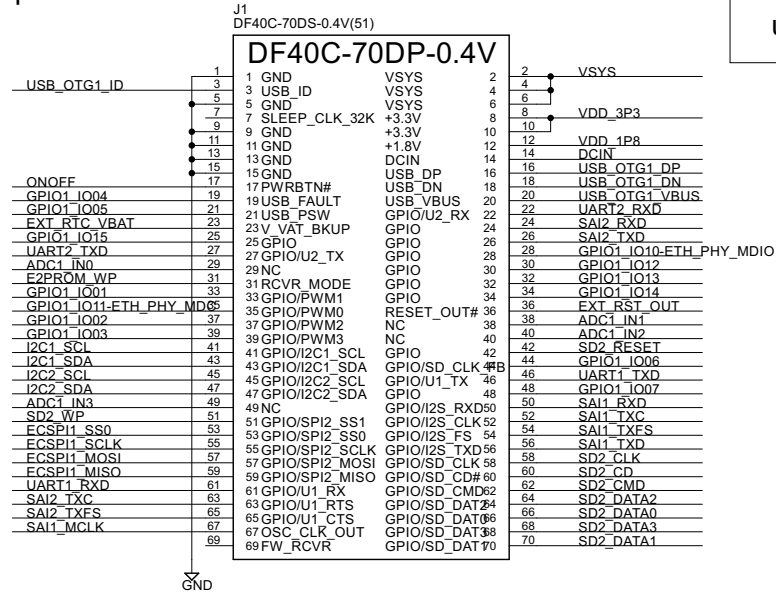
Page 2: uCOM Connectors
 Page 3: MXM3 edge connector
 Page 4: JTAG debug interface and Battery connector
 Page 5: Optional QSPI
 Page 6: Optional LVDS transmitter
 Page 7: Ethernet Interface
 Page 8: Boot Mode Control

UL = UnLoaded = normally not mounted component.

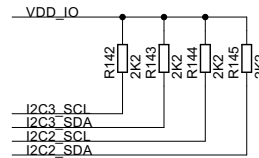
Default jumper settings are indicated in the schematic.
 However, always check jumper positions on actual boards
 since there is no guarantee that all jumpers are in default place.

Rev A1 Corrected Eth-PHY powering: R39=0R and SJ13=UL.	
Rev A Added external PCIe clock connection and I2C pullups.	
Rev PA2 Added PCIe terminations	
Rev PA1 First release	
 <div>(C) Embedded Artists AB</div>	
TITLE: iMX7D uCOM Adapter Board rev A1	
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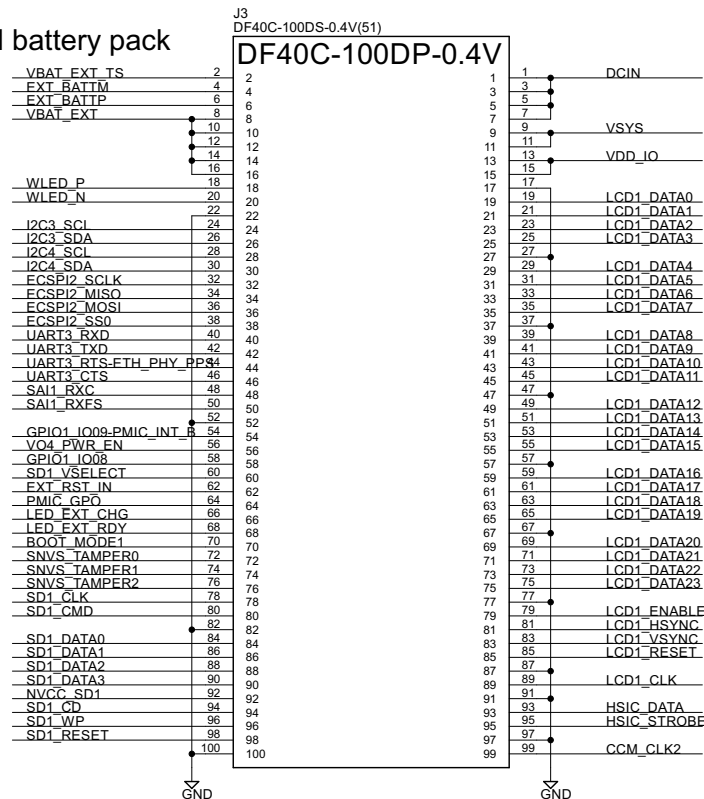
Max 0.3A per pin



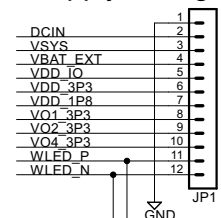
uCOM Connectors



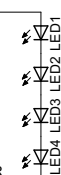
To external battery pack



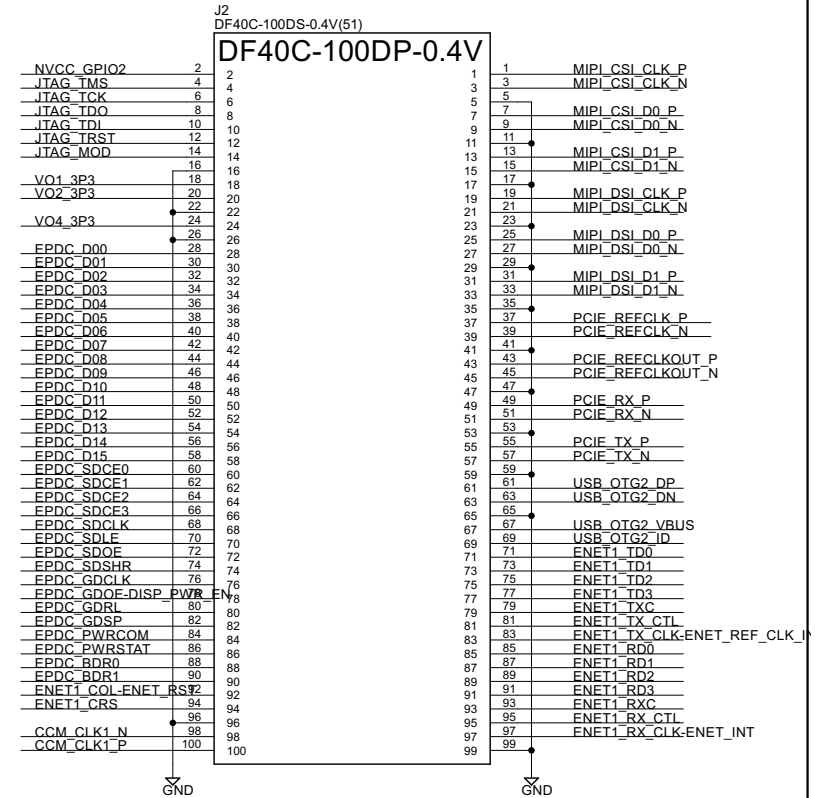
Supply Voltages



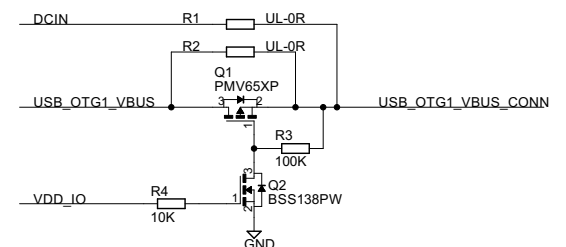
White LEDs



uCOM Connectors



USB OTG VBUS



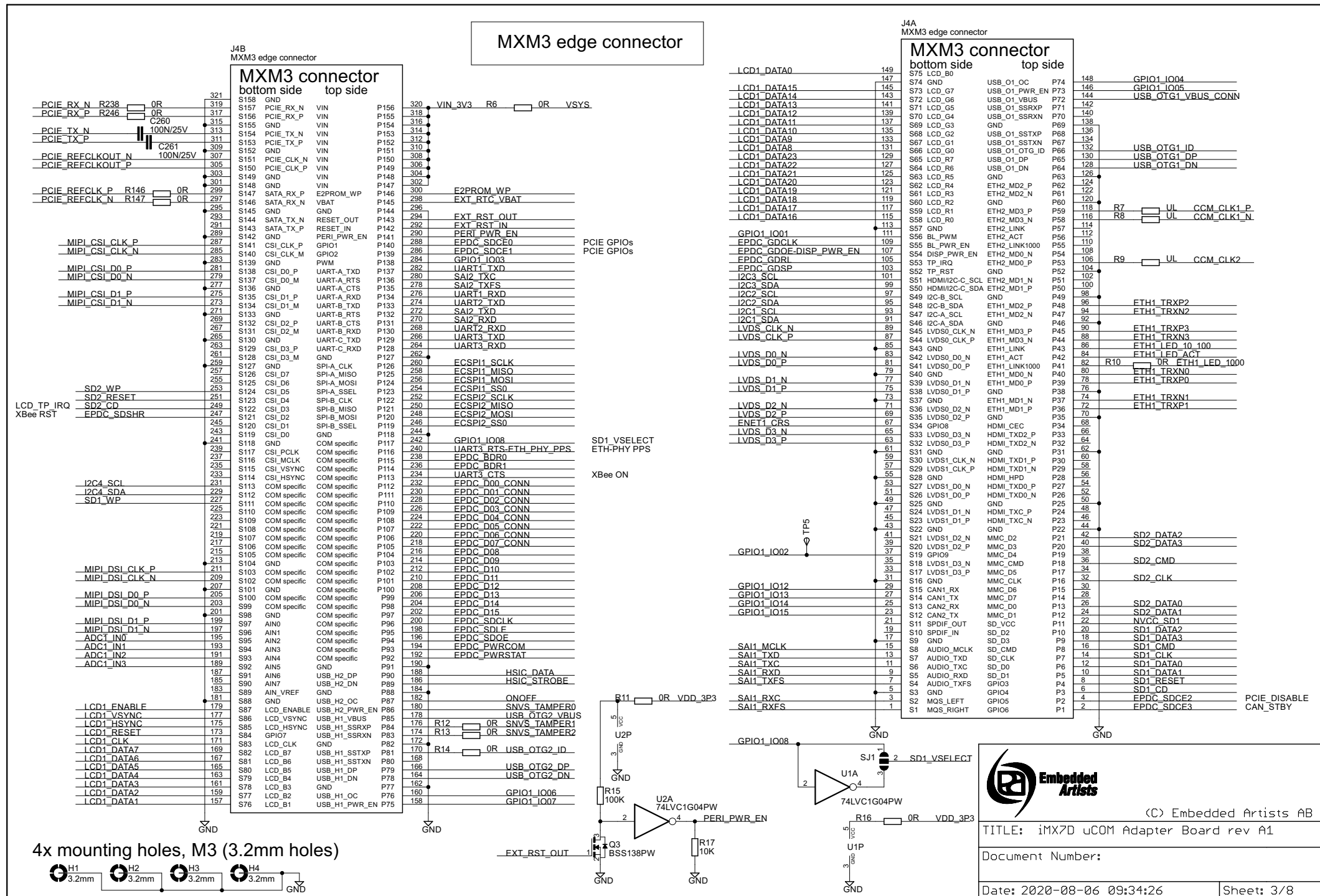
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TITLE: iMX7D uCOM Adapter Board rev A1

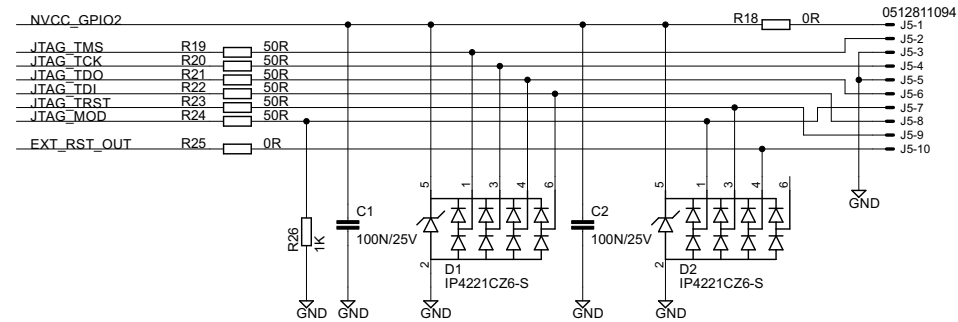
Document Number:

Date: 2020-08-06 09:34:26

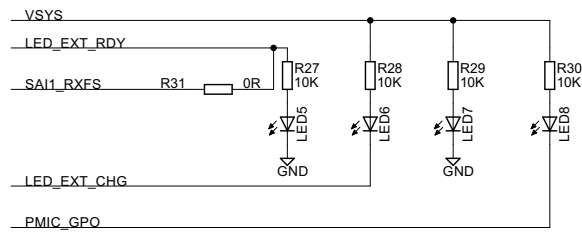
Sheet: 2/8



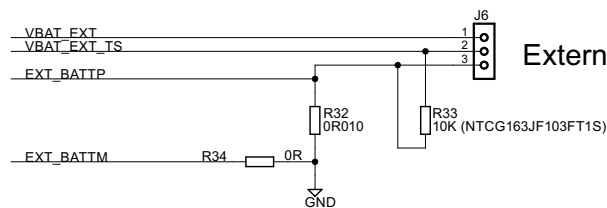
JTAG debug interface and Battery connector



JTAG Debug Interface



PMIC indicators



External battery connector



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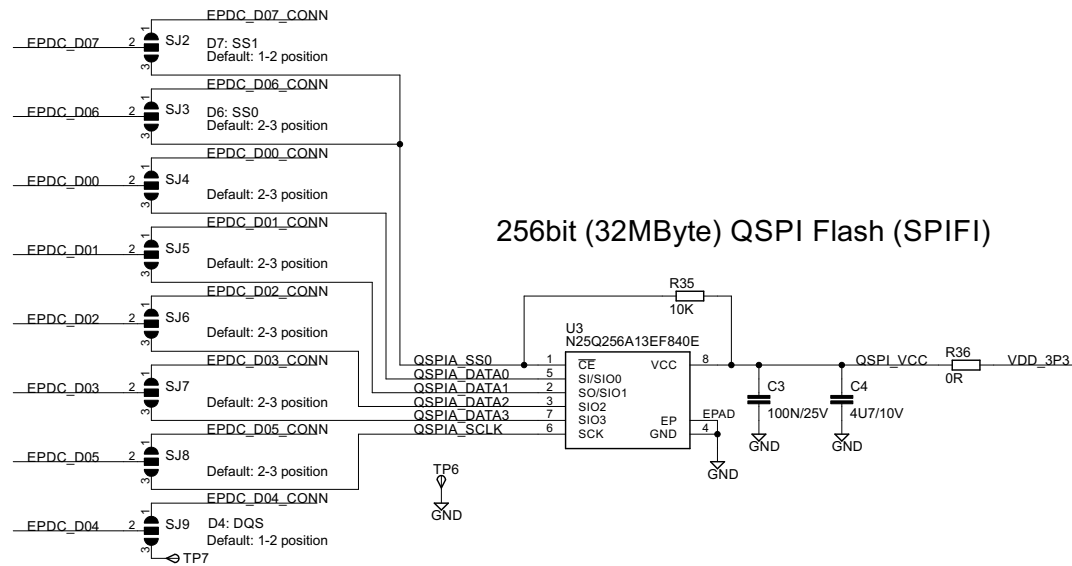
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QSPI



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TITLE: iMX7D uCOM Adapter Board rev A1

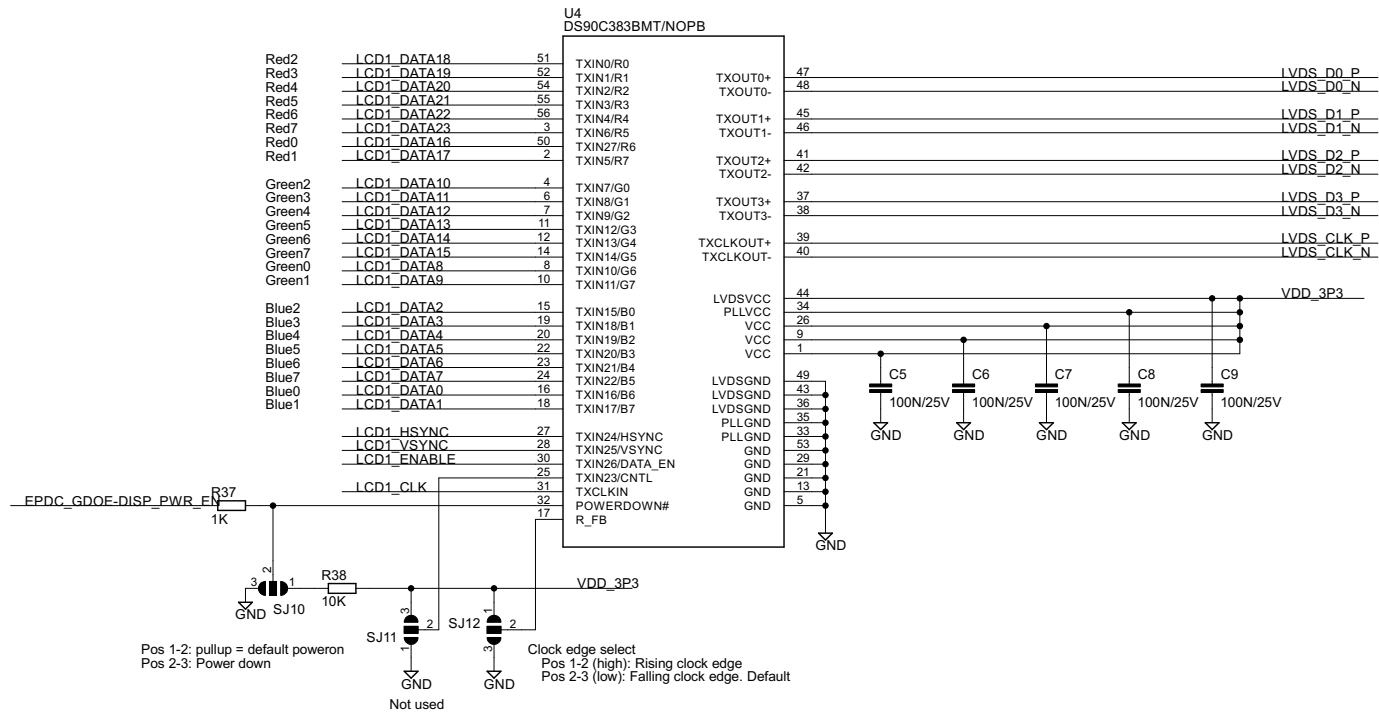
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Optional LVDS transmitter

SPWG/PSWG/VESA 18/24 bpp Data Mapping



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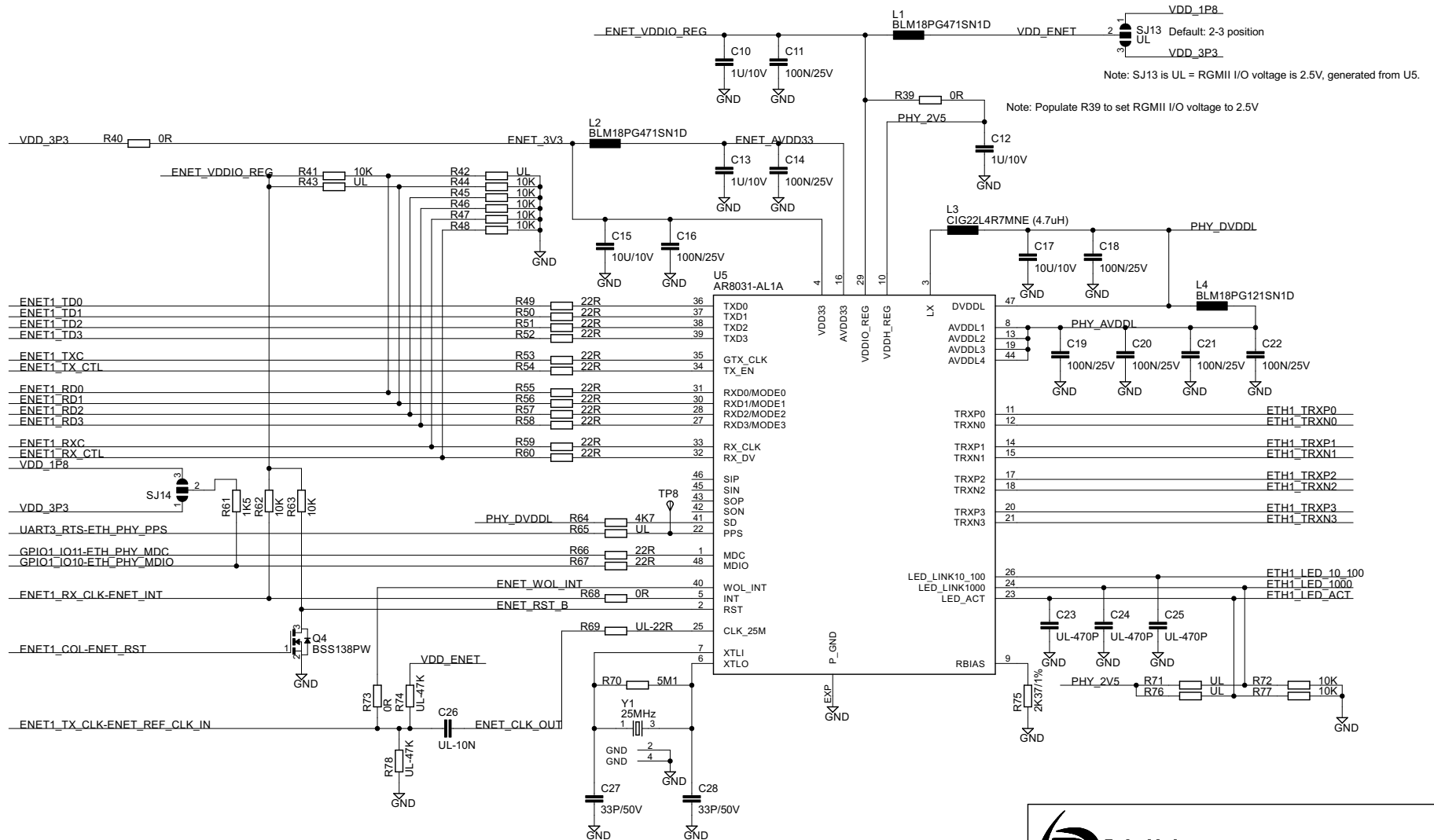
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Ethernet Interface



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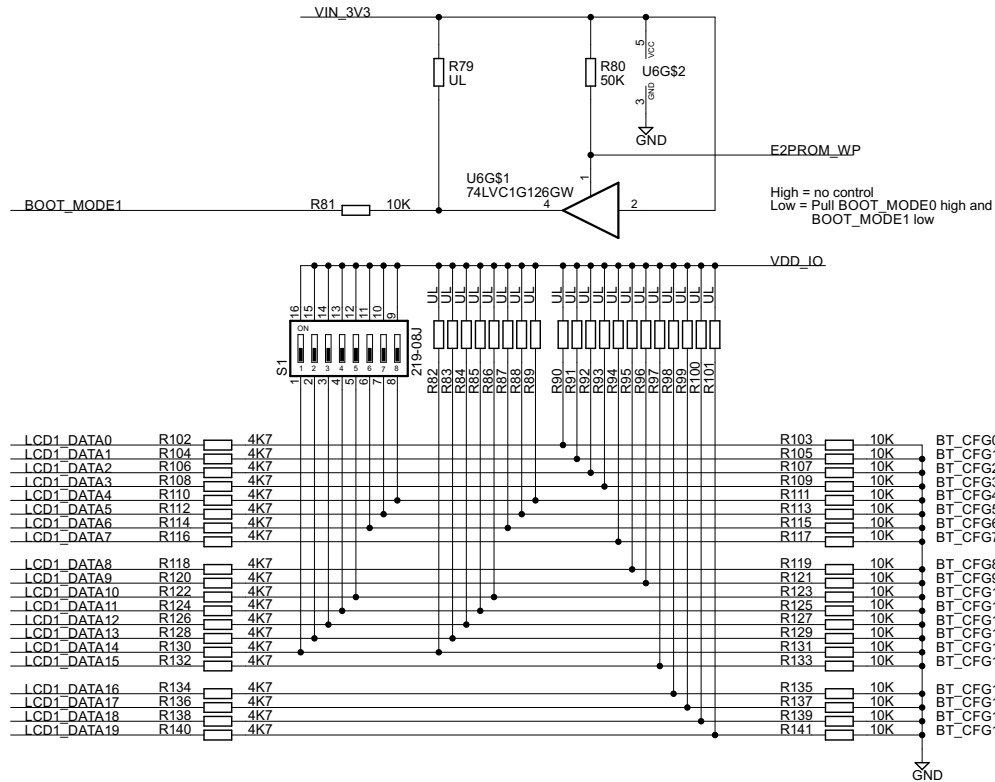
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Boot Mode Control



Boot Mode

	BOOT_MODE1	BOOT_MODE0
Fuses	Low	Low
USB OTG	Low	High
Internal boot	High	Low
Test mode	High	High

Activated by pulling E2PROM_WP low
Default, resistors set to eMMC boot on SD3

	BT_CFG14	BT_CFG13	BT_CFG12	BT_CFG11	BT_CFG10	BT_CFG6	BT_CFG5	BT_CFG4
SD boot	0	0	1	Port Select: 00: eSDHC1 01: eSDHC2 10: eSDHC3		0	0	Bus width: 0: 1-bit 1: 4-bit
MMC/eMMC boot	0	1	0			Bus width: 000: 1-bit 001: 4-bit 010: 8-bit 101: 4-bit DDR (MMC 4.4) 110: 8-bit DDR (MMC 4.4)		
QSPI boot	1	0	0	0	0	0	0	0



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