

To Our Valued Customers,

This PCN affects our iMX7 Dual uCOM board (product number: EAC00266 and EAC00267).

The ARM/SOC operating voltage of the iMX7Dual application processor should be increased by 100mV to get robust and reliable operation over the LPDDR3 memory bus interface. This update is recommended for all boards produced.

In the DTS file, there are two rows that shall be updated. I have direct links to our latest release below. It looks the same on previous Linux releases also.

https://github.com/embeddedartists/linux-imx/blob/ea_5.10.35/arch/arm/boot/dts/imx7dea-com.dtsi#L79

https://github.com/embeddedartists/linux-imx/blob/ea_5.10.35/arch/arm/boot/dts/imx7dea-com.dtsi#L81

Please update as below on row 79 and 81. The two "-" rows shall be replaced by the "+" rows below:

```
bd7181x,pmic-buck1-uses-i2c-dvs;
-   bd7181x,pmic-buck1-dvs-voltage = <1150000>, <950000>; /* VDD_ARM */
+   bd7181x,pmic-buck1-dvs-voltage = <1250000>, <1000000>; /* VDD_ARM */
bd7181x,pmic-buck2-uses-i2c-dvs;
-   bd7181x,pmic-buck2-dvs-voltage = <1150000>, <950000>; /* VDD_SOC */
+   bd7181x,pmic-buck2-dvs-voltage = <1250000>, <1000000>; /* VDD_SOC */
```

The update is only needed on the Linux DTS. No update is needed on the uBoot DTS.

Kind Regards,
Embedded Artists AB
December 10th, 2021